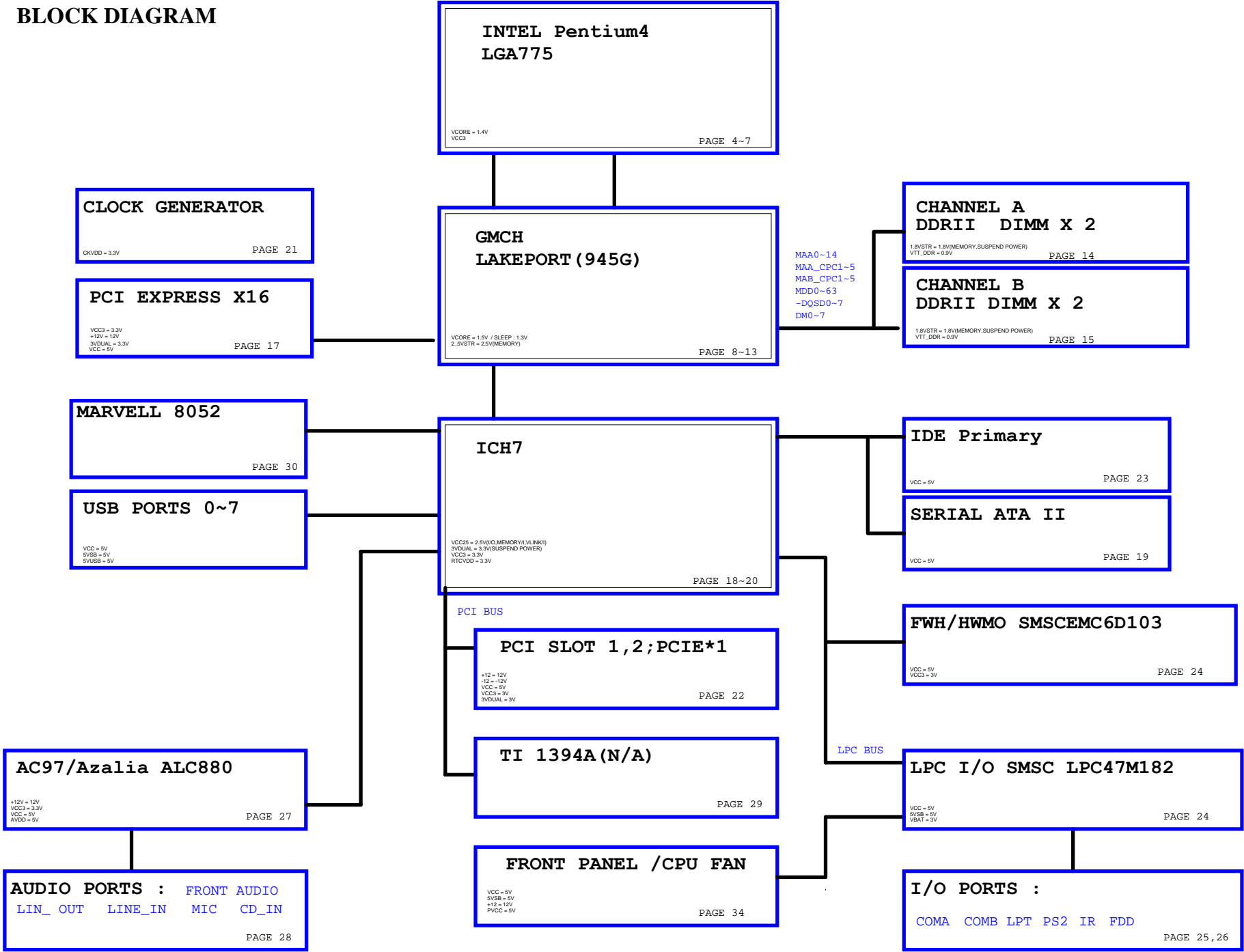


01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4 LGA775 A
05	P4 LGA775 B
06	P4 LGA775 C
07	P4 LGA775 D,E,F,G
08	GMCH-LAKEPORT HOST
09	GMCH-LAKEPORT DDRII
10	GMCH-LAKEPORT PCI E, DMI
11	GMCH-LAKEPORT INT VGA
12	GMCH-LAKEPORT GND
13	GMCH-LAKEPORT PWR
14	DDRII CHANNEL A 1,2
15	DDRII CHANNEL B 1,2
16	DDRII TERMINATION
17	PCI EXPRESS*16 SLOT
18	ICH7 PCI, USB, DMI, LAN
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
21	GB/CK410M CLOCK.
22	PCI SLOT 1,2,PCIE*1
23	IDE/FLOPPY/USB
24	SMSC LPC IO
25	COM LPT
26	BIOS,CI,HWM,KB/MS
27	ALC880

SHEET	TITLE
28	REAR AUDIO JACK
29	TI TSB43AB23 1394
30	MARVELL 88E8052
31	VCORE PWM ISL6556
32	DISCRETE POWER
33	ATX, OTHERS POWER
34	FRONT PANEL

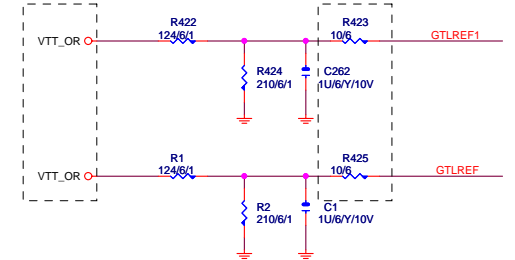
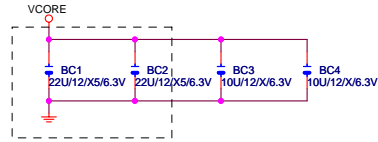
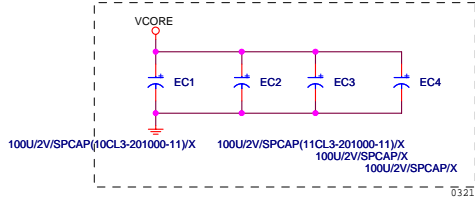
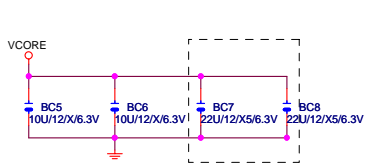
BLOCK DIAGRAM



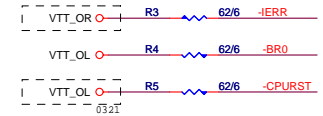
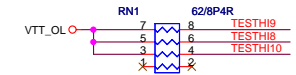
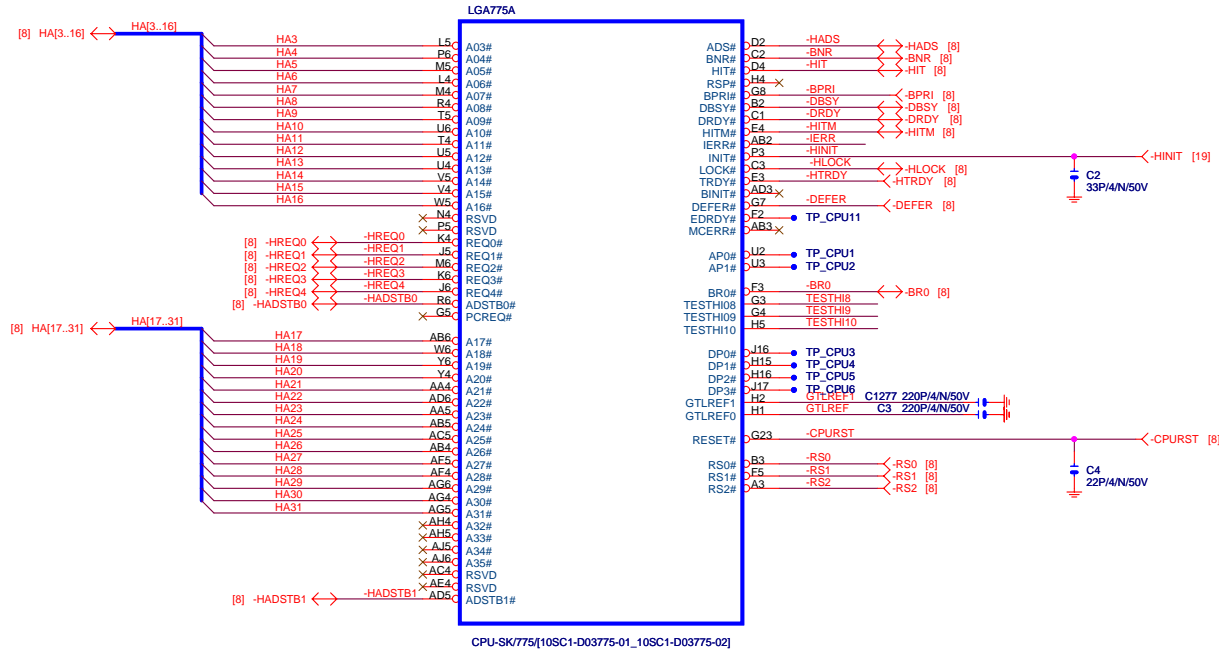
Circuit or PCB layout change
for next version

2005/06/10

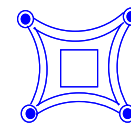
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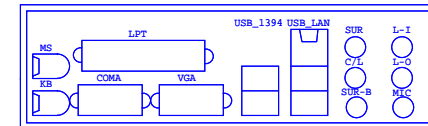
LGA775 FOOT PRINT FOR ACER:LGA775-ACER

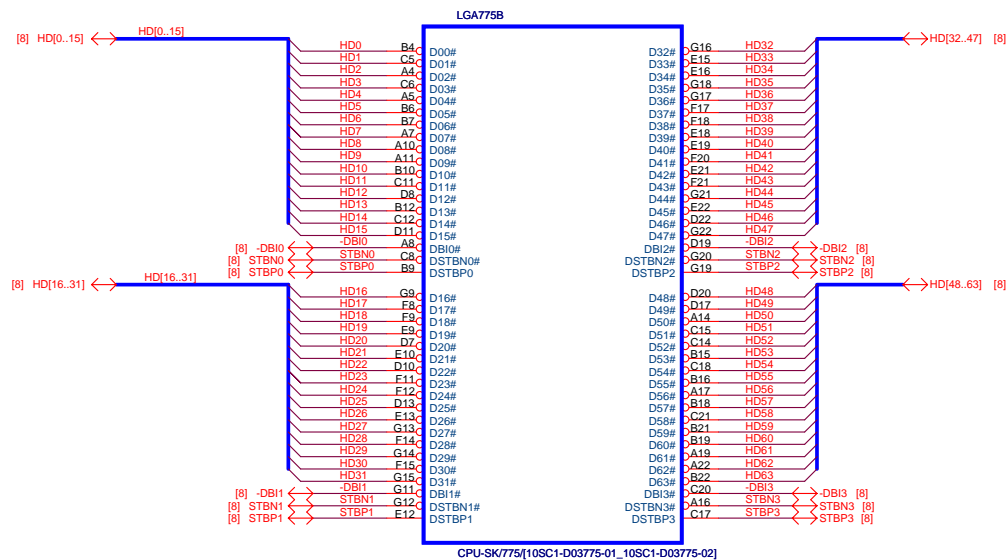


LGA775-CR
CPU_RETAINION[12KRC-040005-21]

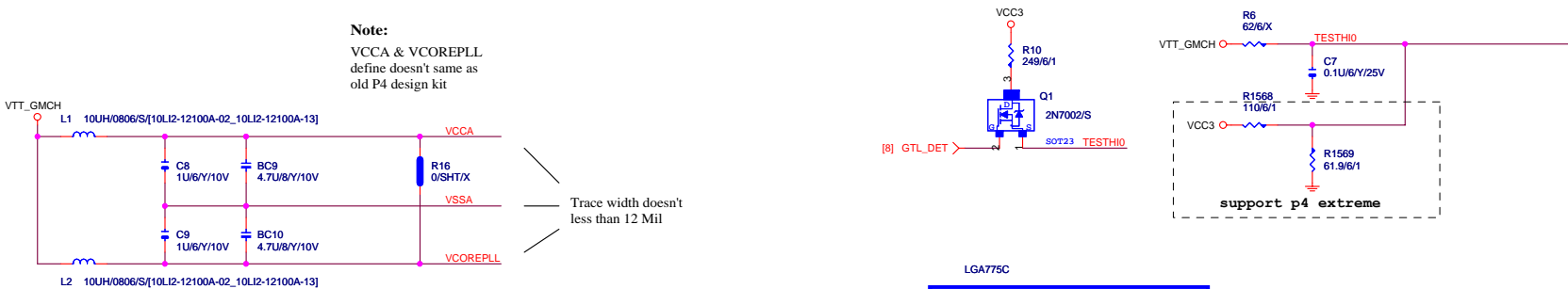


IO_SHIELD
IO_SHIELD[12AIO-AE0006-01]

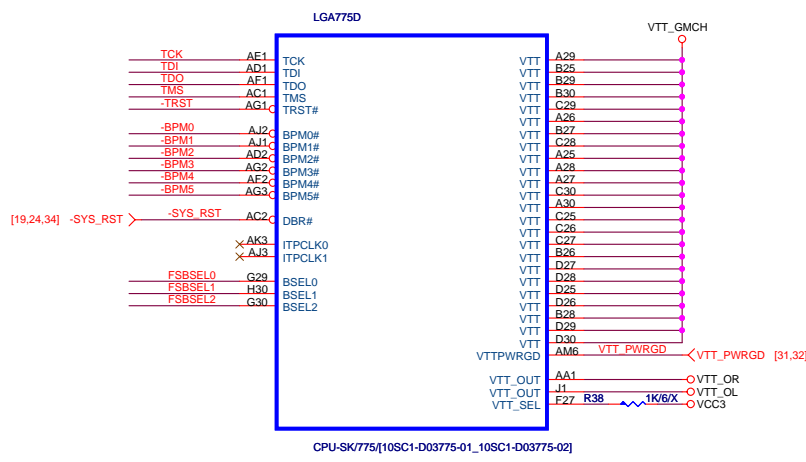
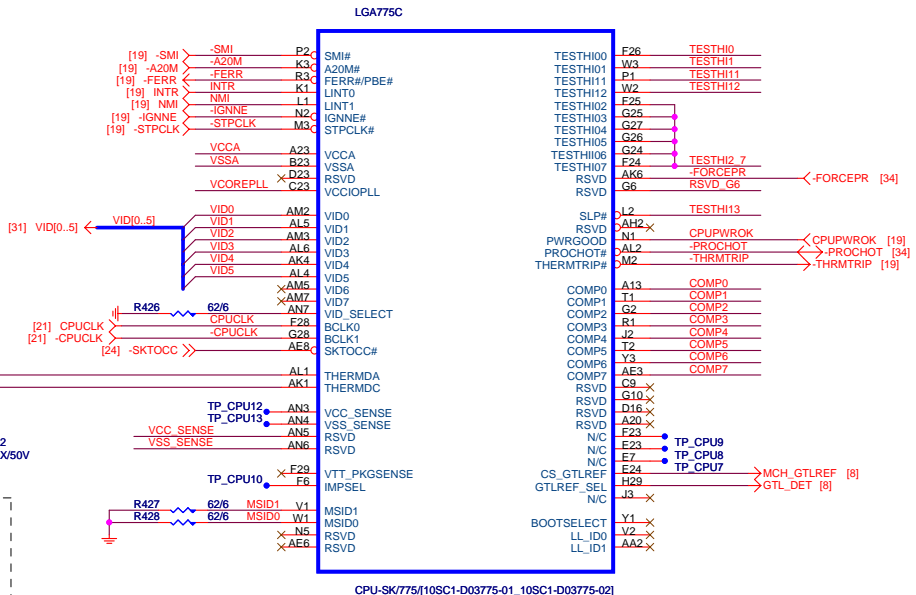
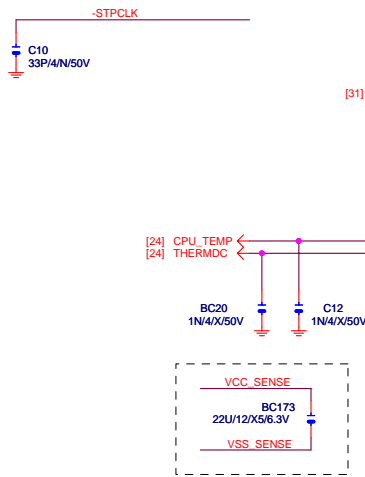




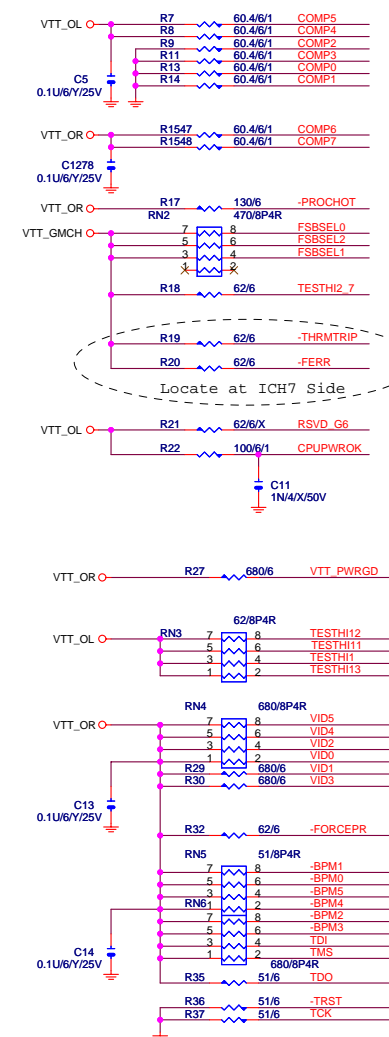
Note:
VCCA & VCOREPLL
define doesn't same as
old P4 design kit



As close as possible to
CPU socket

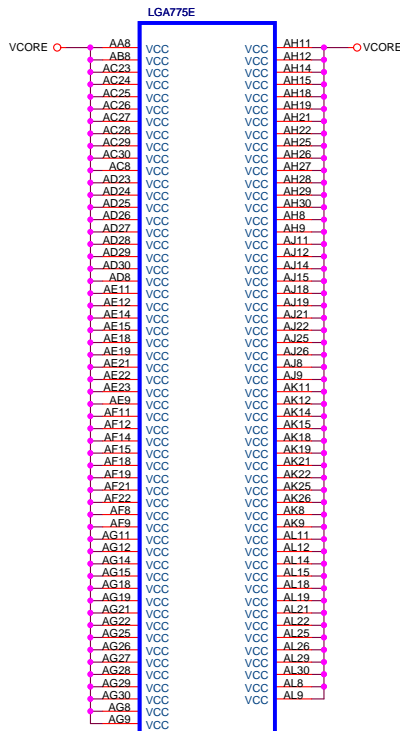


Place outside of CPU socket

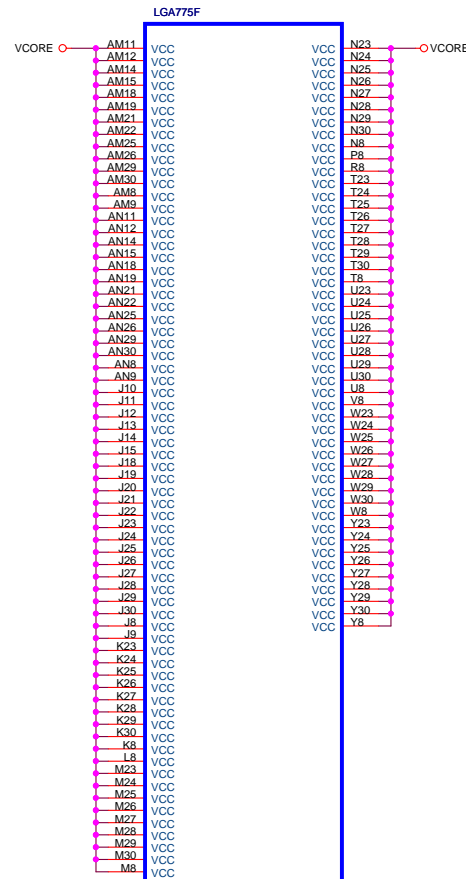


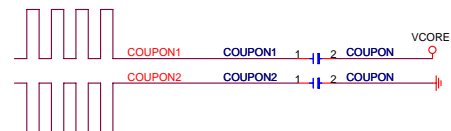
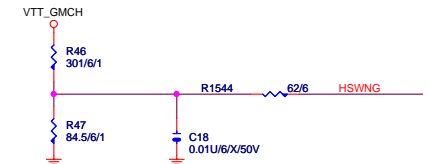
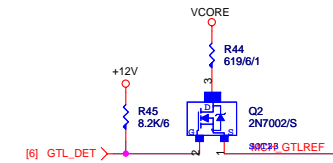
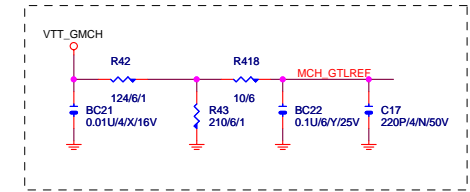
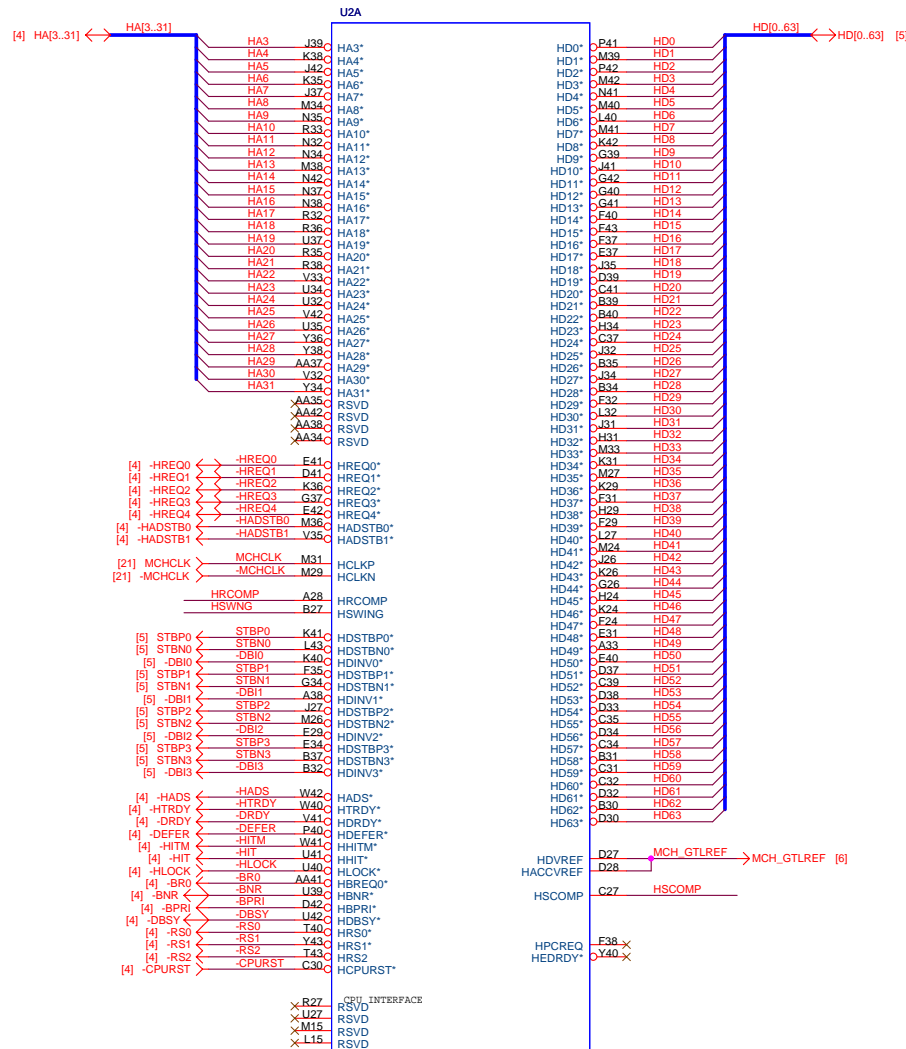
Gigabyte Technology

Title			P4_LGA775-B	
Size			81945AE-AE	
Document Number			Rev 1.1	
Date: Friday, June 10, 2005			Sheet 6 of 34	

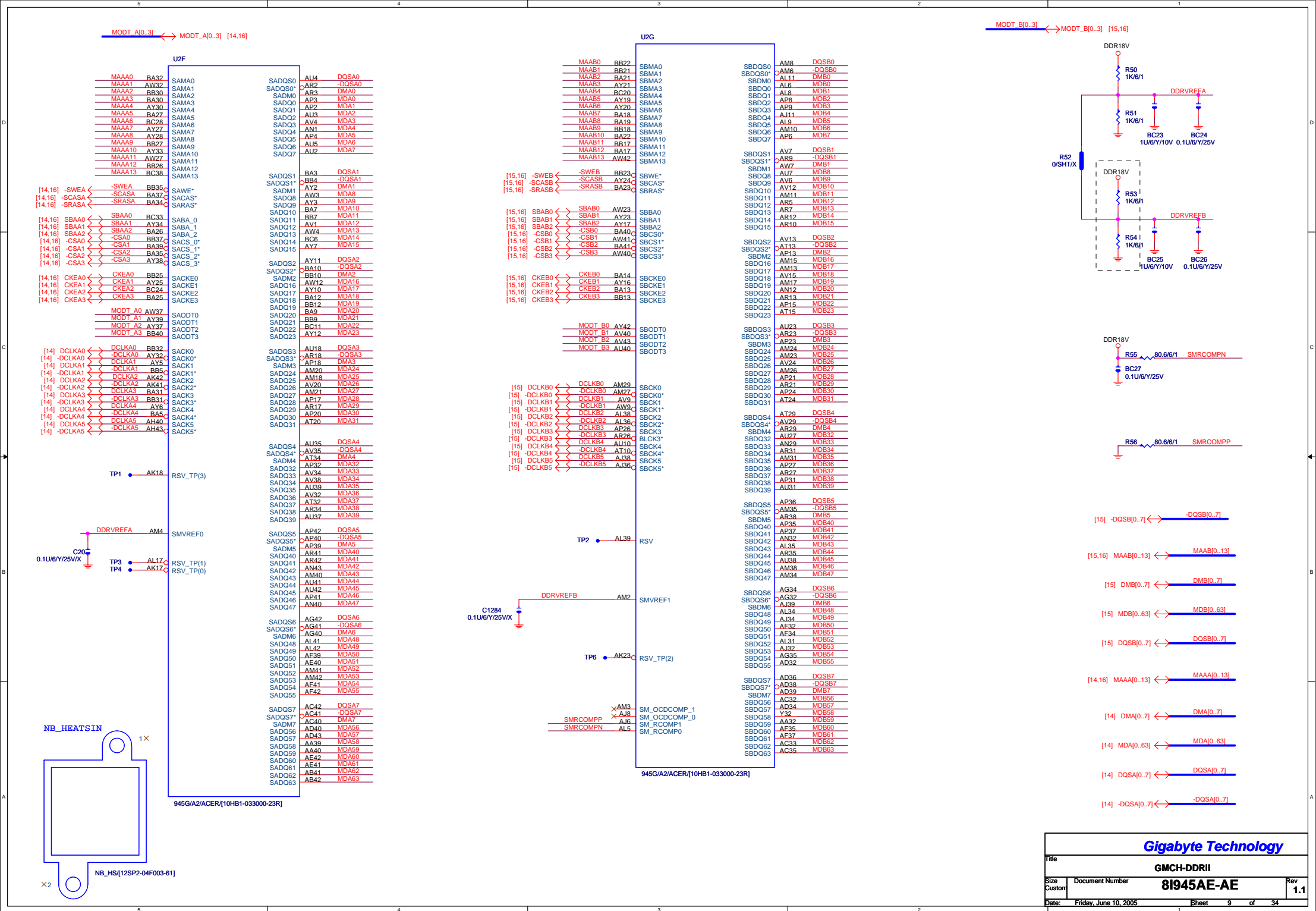


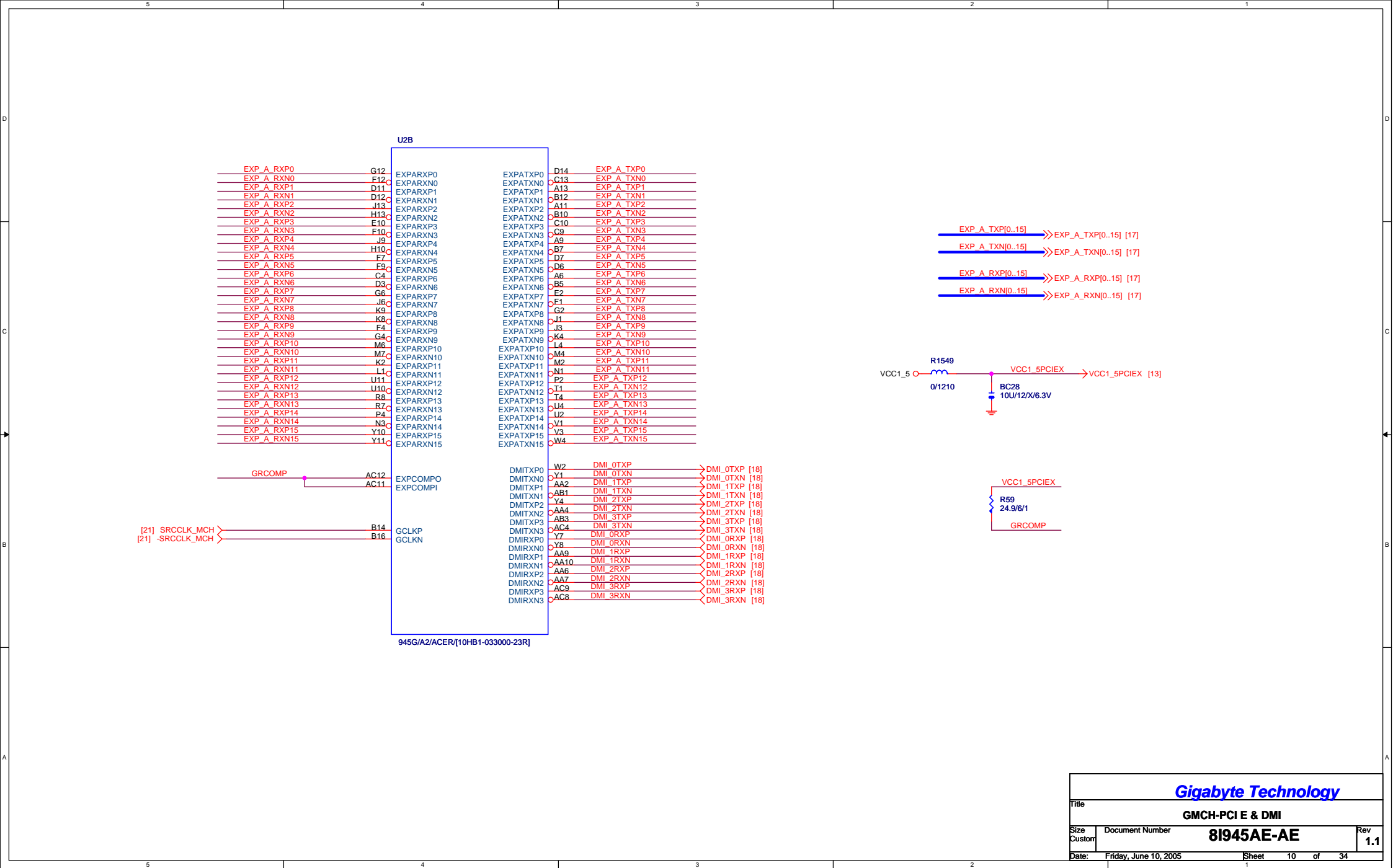
CPU-SK/775[10SC1-D03775-01_10SC1-D03775-02]





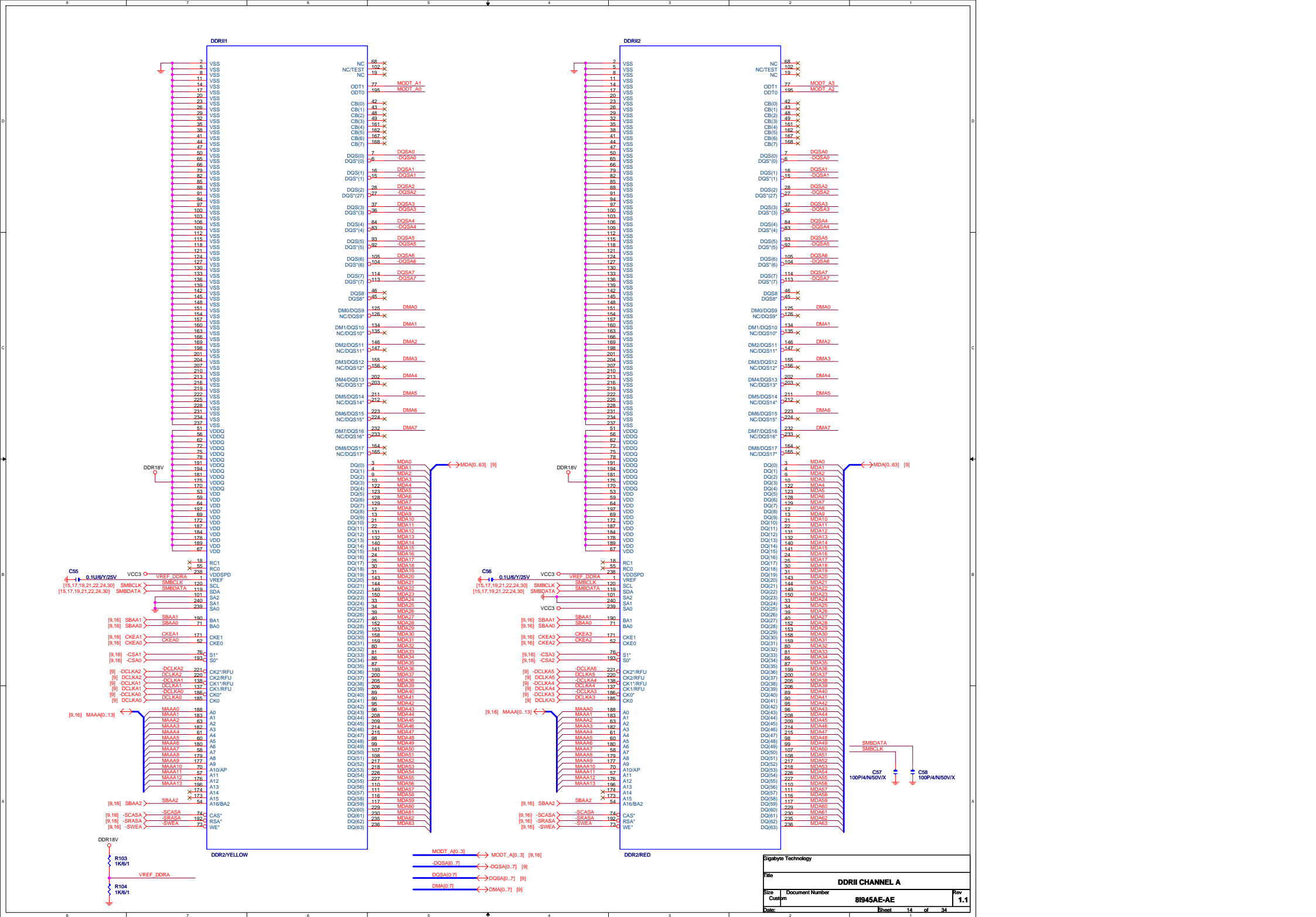
Gigabyte Technology			
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GMCH-HOST			
Size	Document Number	81945AE-AE	Rev
Custom			1.1
Date:	Friday, June 10, 2005	Sheet	8 of 34

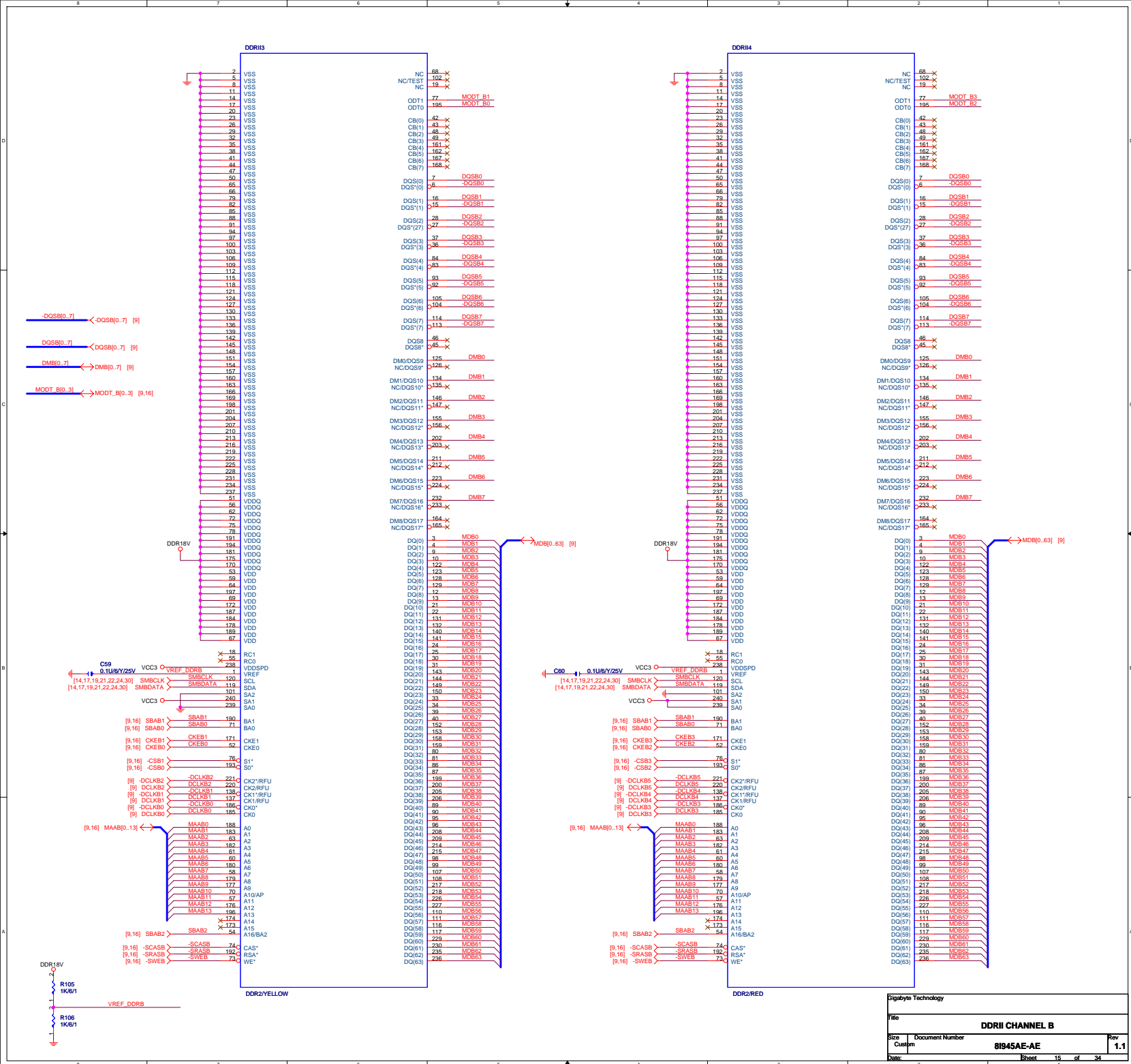




Gigabyte Technology

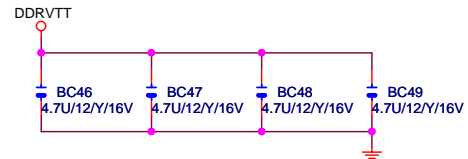
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GMCH-PCI E & DMI		
Size	Document Number	Rev
Custom	8I945AE-AE	1.1
Date:	Friday, June 10, 2005	Sheet 10 of 34



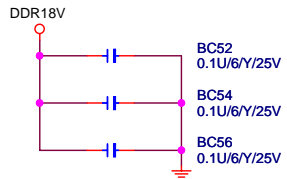


DDR TERMINATION CHANNEL A

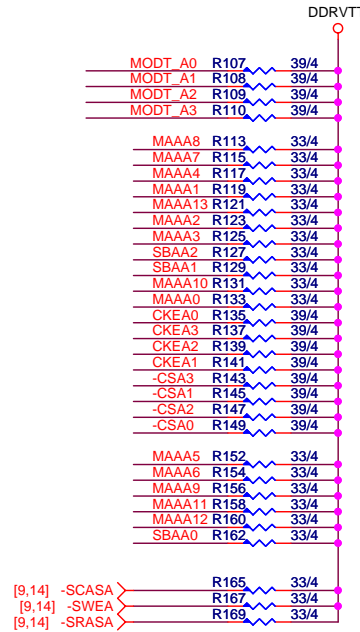
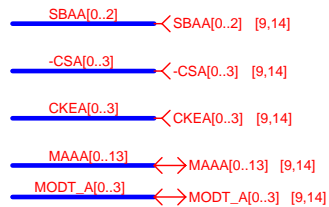
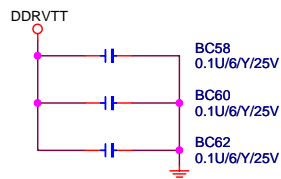
DDRVTT Decouple



DDR18V Decouple

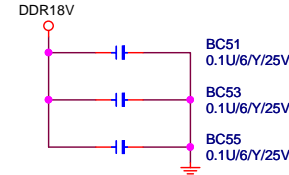


DDRVTT Decouple

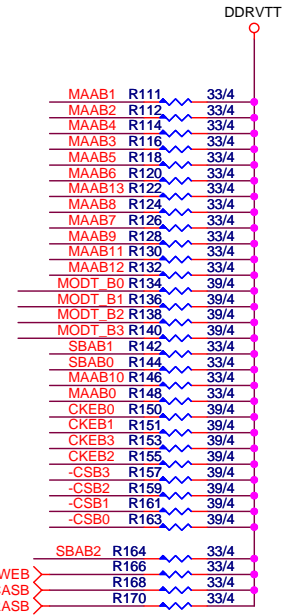
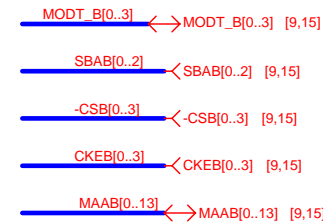
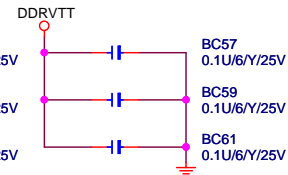


DDR TERMINATION CHANNEL B

DDR18V Decouple

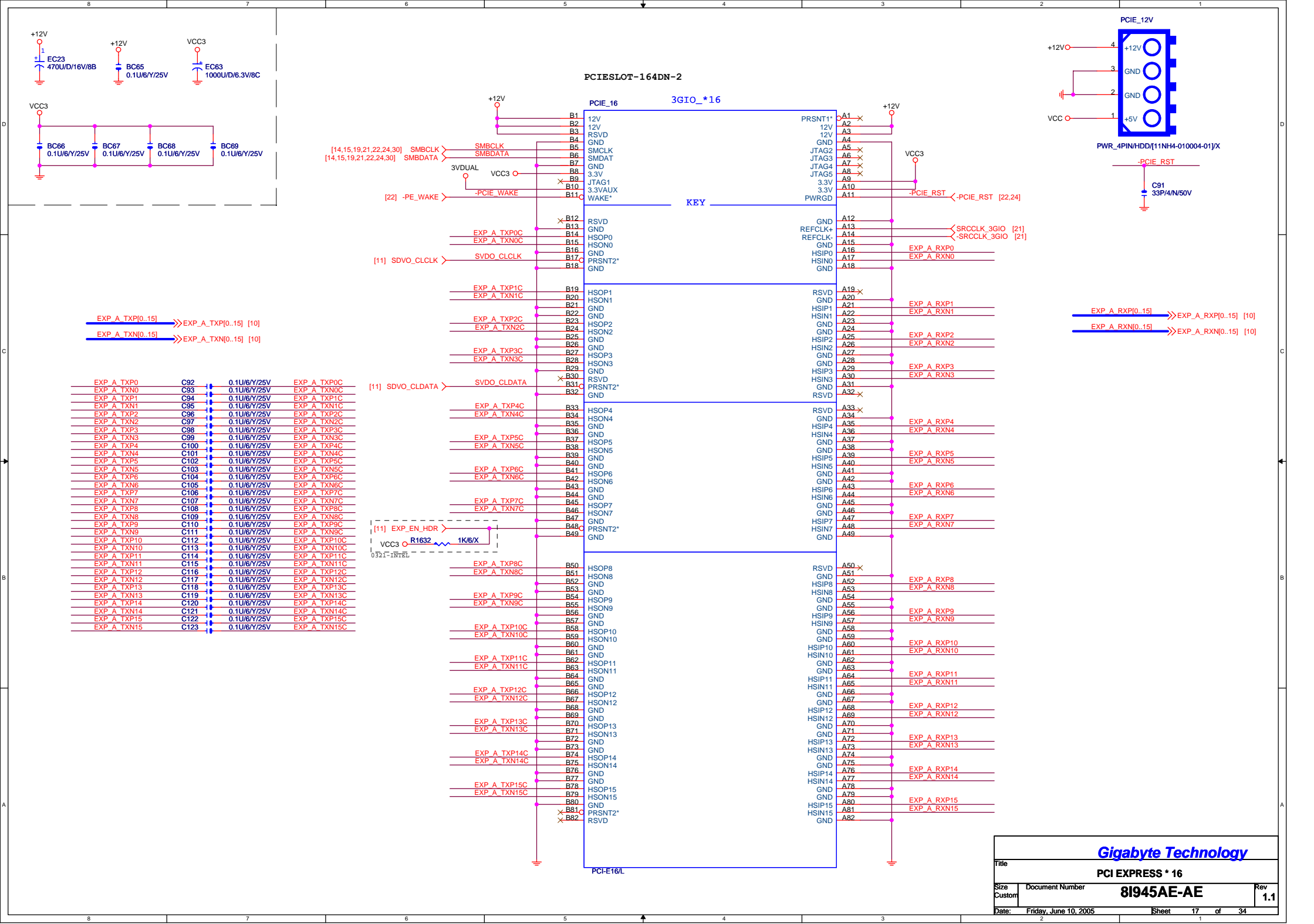


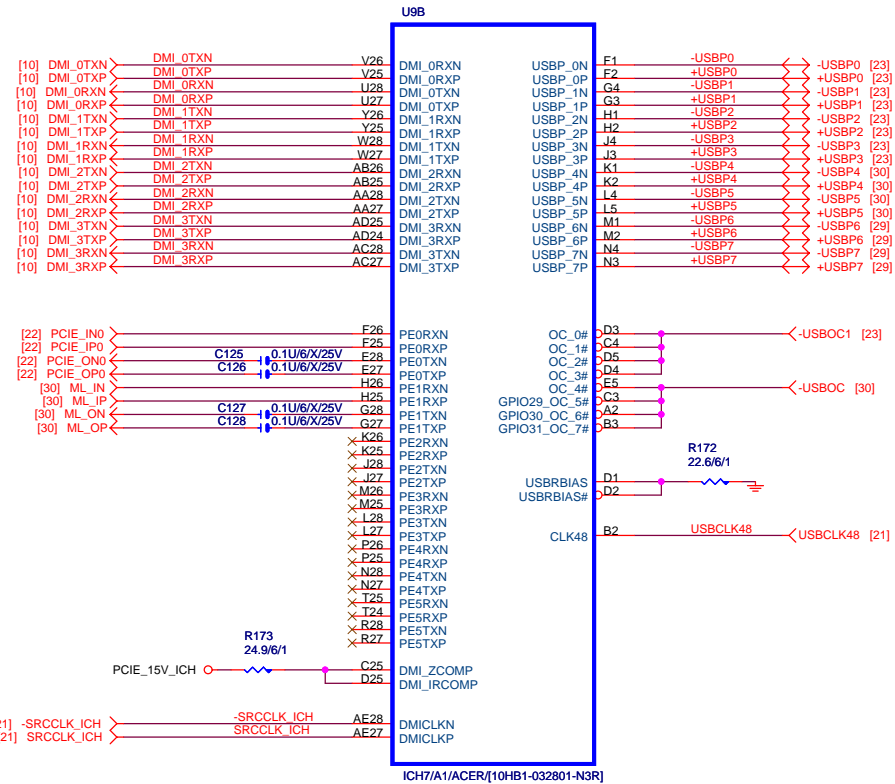
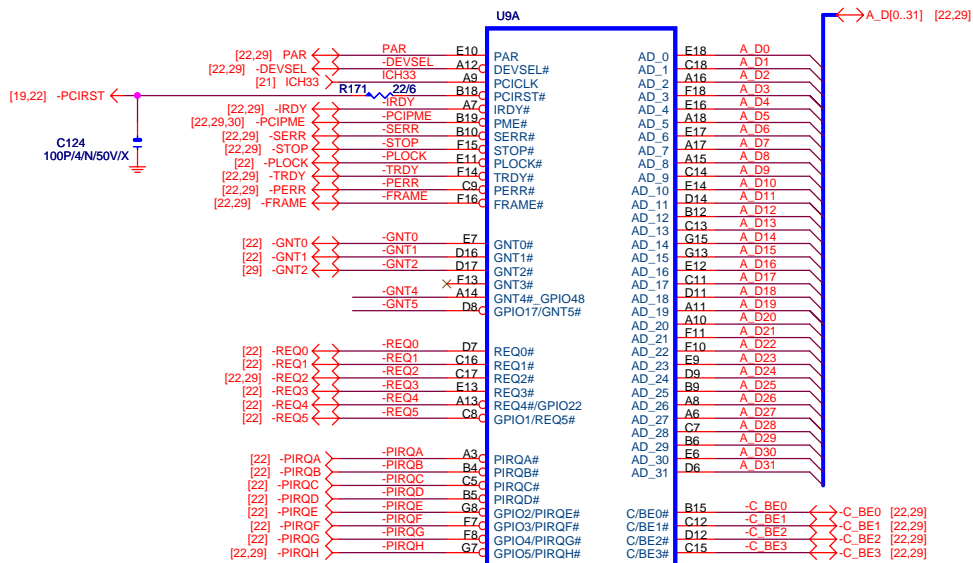
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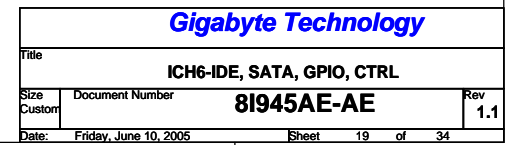


Gigabyte Technology

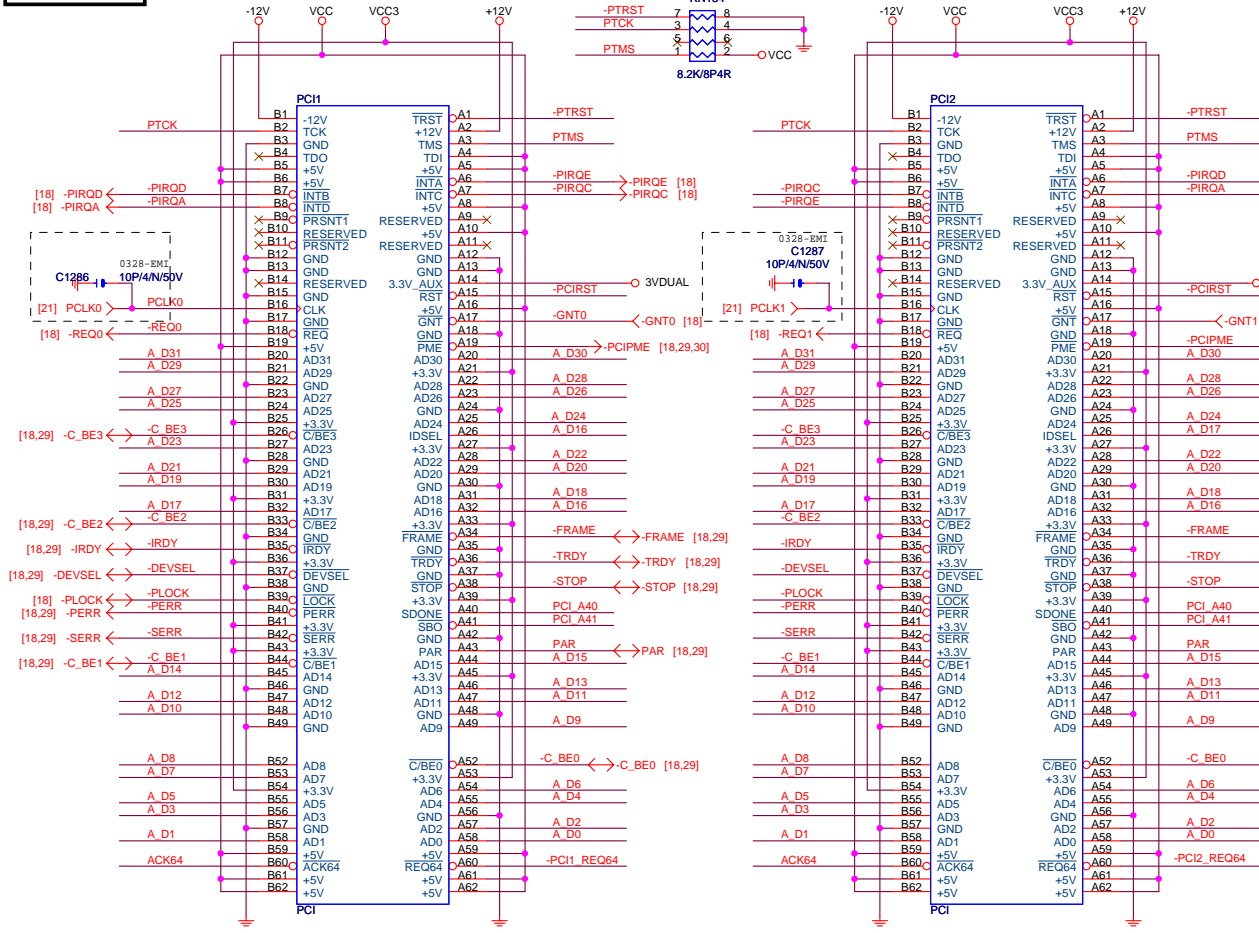
DDRII TERMINATOR



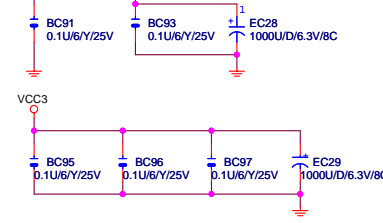
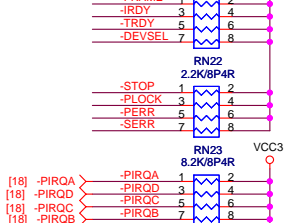
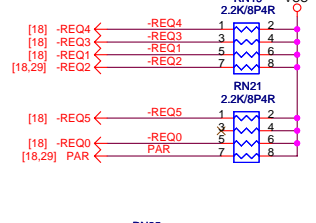
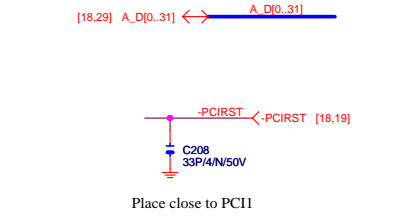
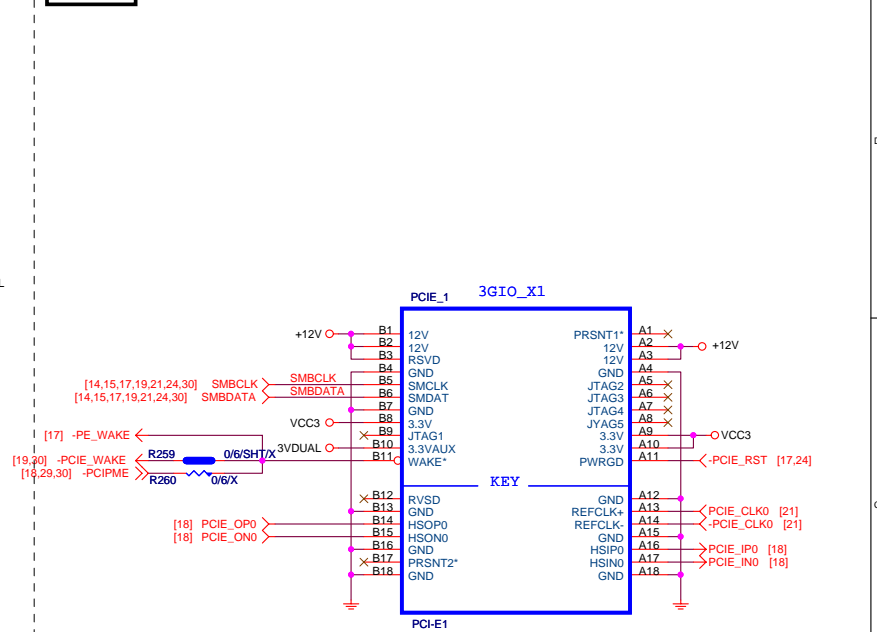




PCI1, 2 SLOT

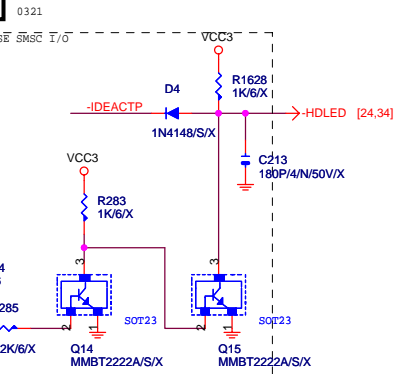


PCIE*1

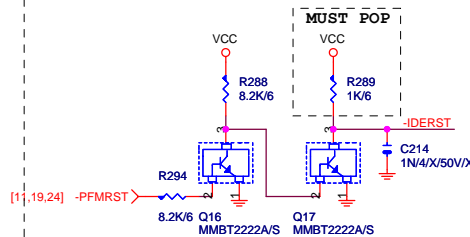


Gigabyte Technology			
PCI SLOT 1, 2/PCIE*1			
8I945AE-AE			
Rev	1.1		
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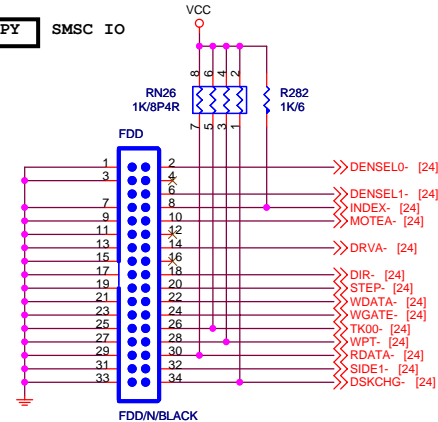
IDE/SATA LED



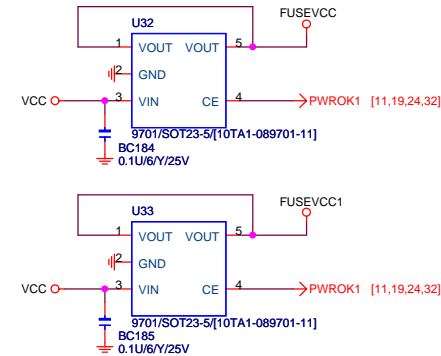
IDE RESET



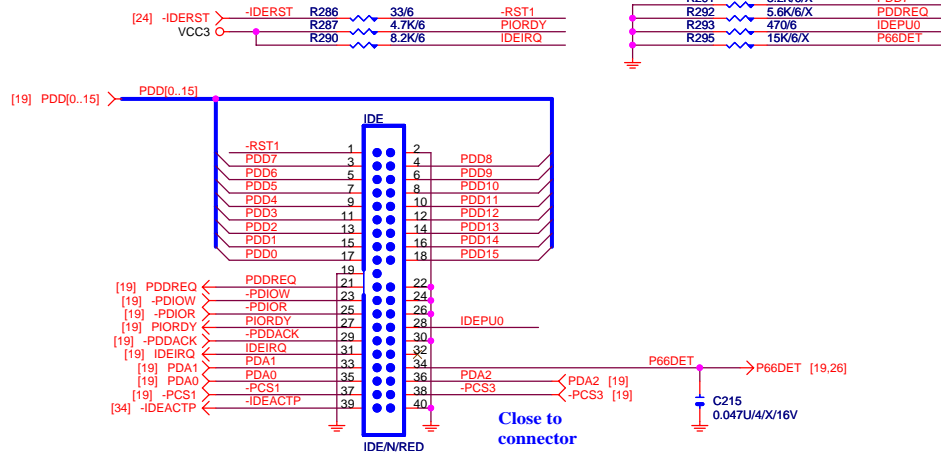
FLOPPY SMSC IO



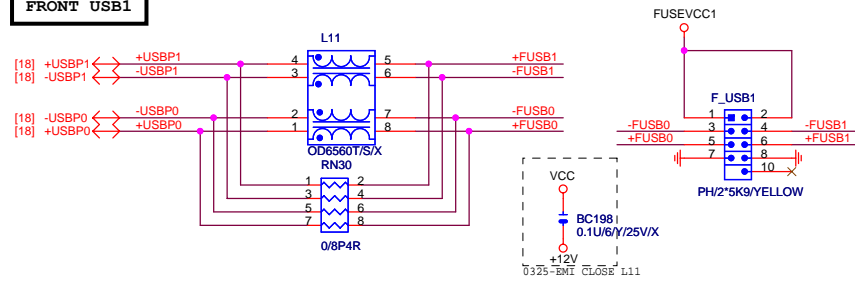
USB DROP



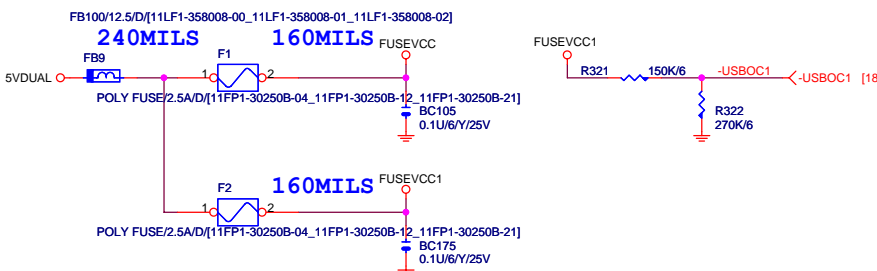
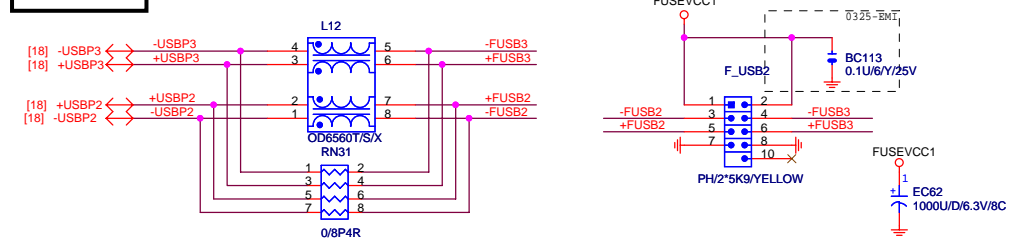
IDE



FRONT USB1

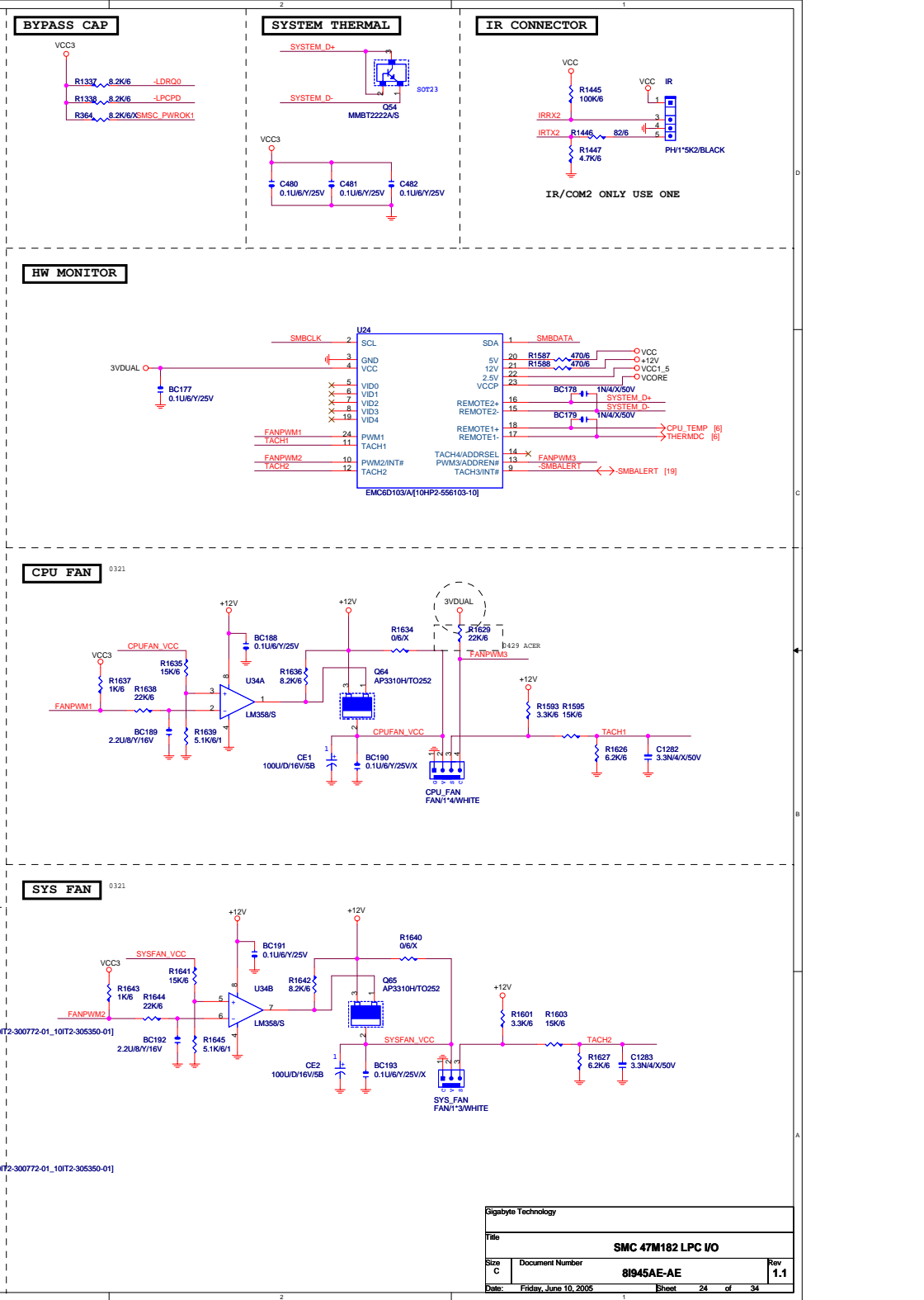
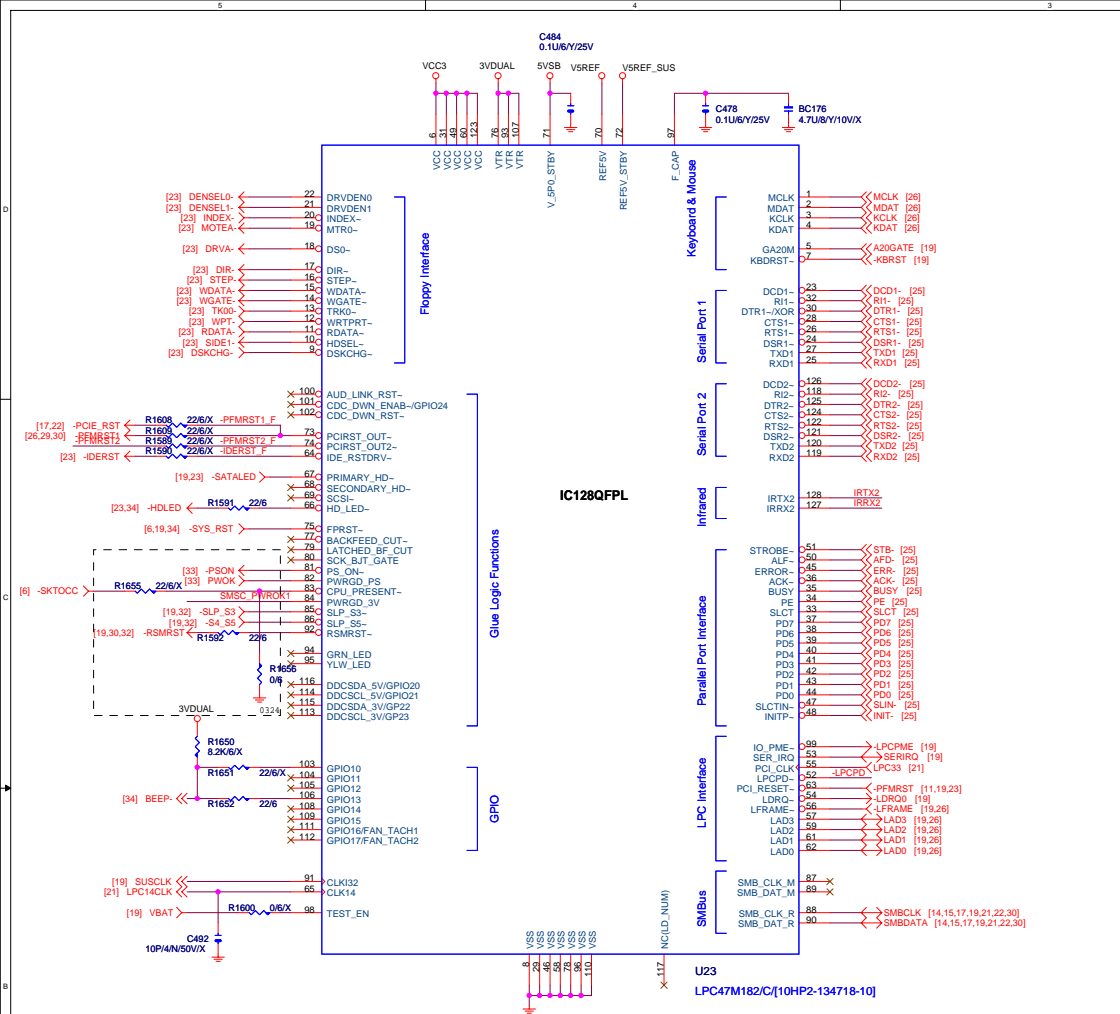


FRONT USB2

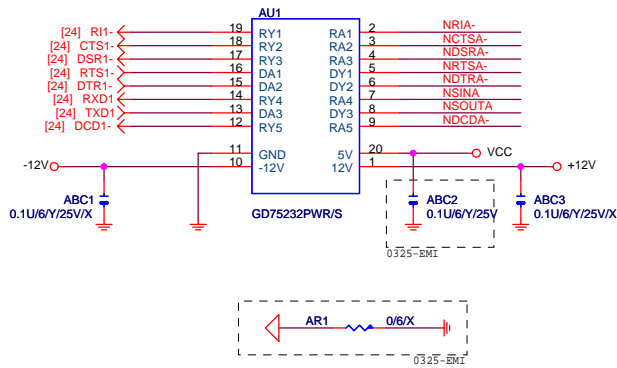


Gigabyte Technology

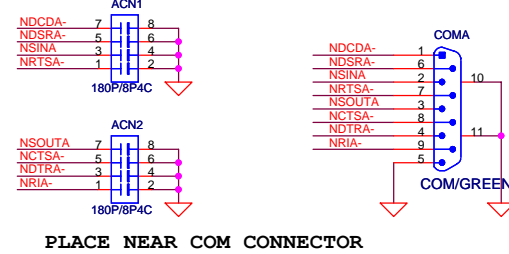
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Size	Document Number	Rev
B	81945AE-AE	1.1
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COMA

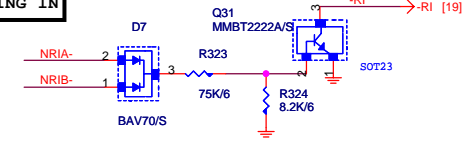


EXTERNAL COMA

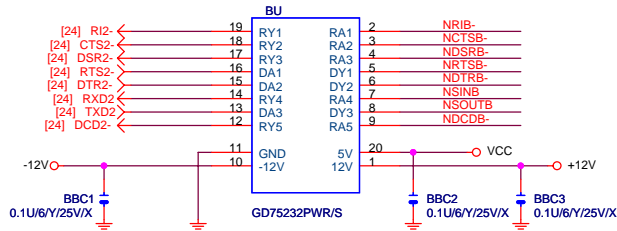


PLACE NEAR COM CONNECTOR

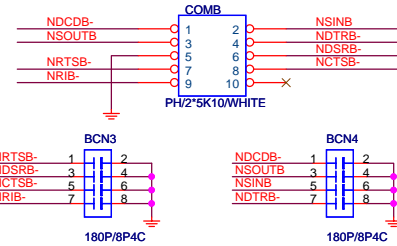
RING IN



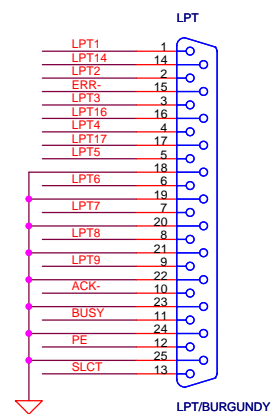
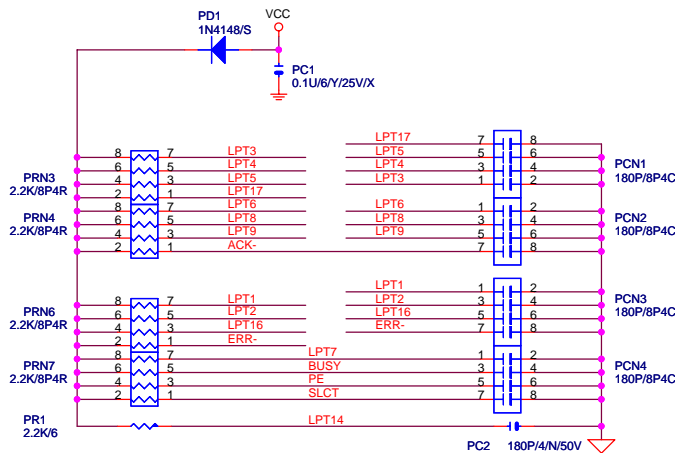
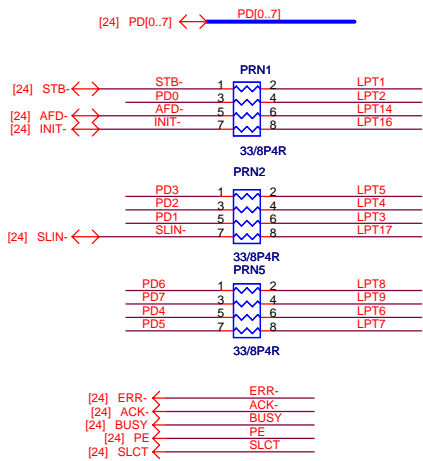
COMB



INTERNAL COMB

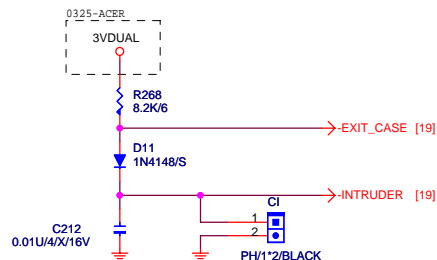


LPT PORT

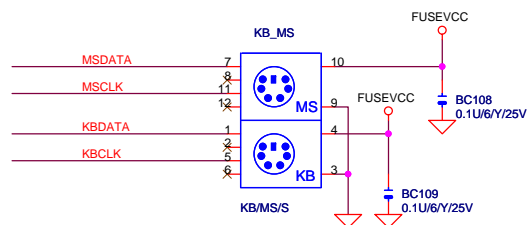
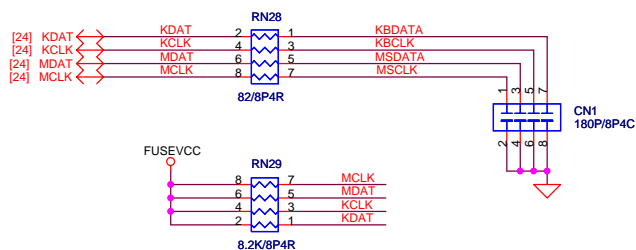


CASE OPEN

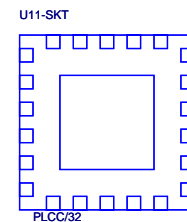
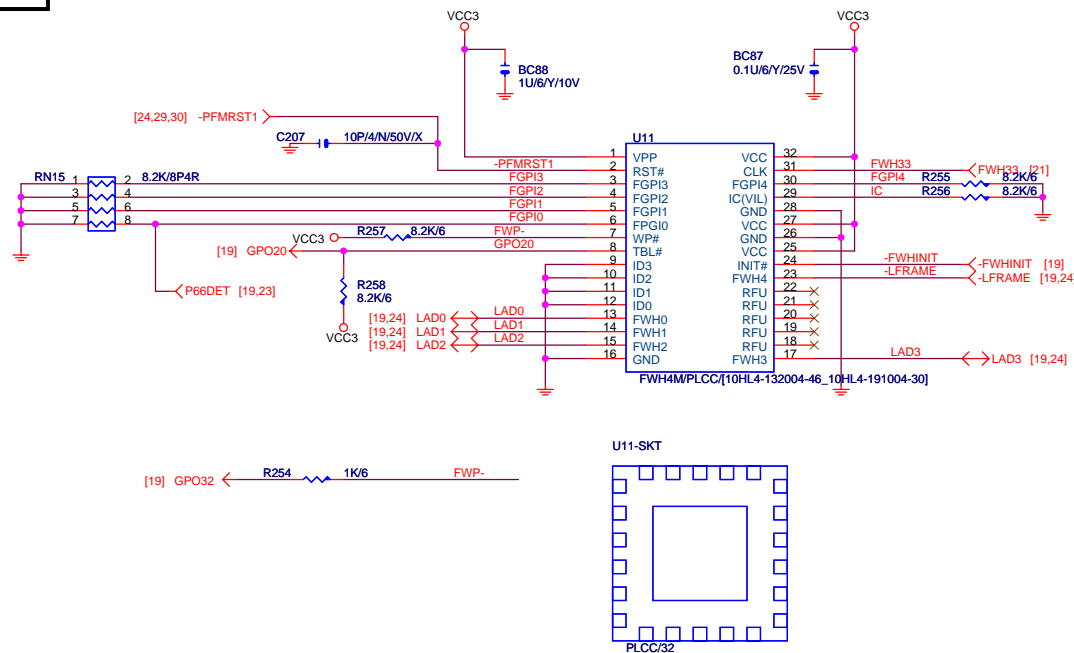
CASE OPEN=LOW;
CASE CLOSE=HI;



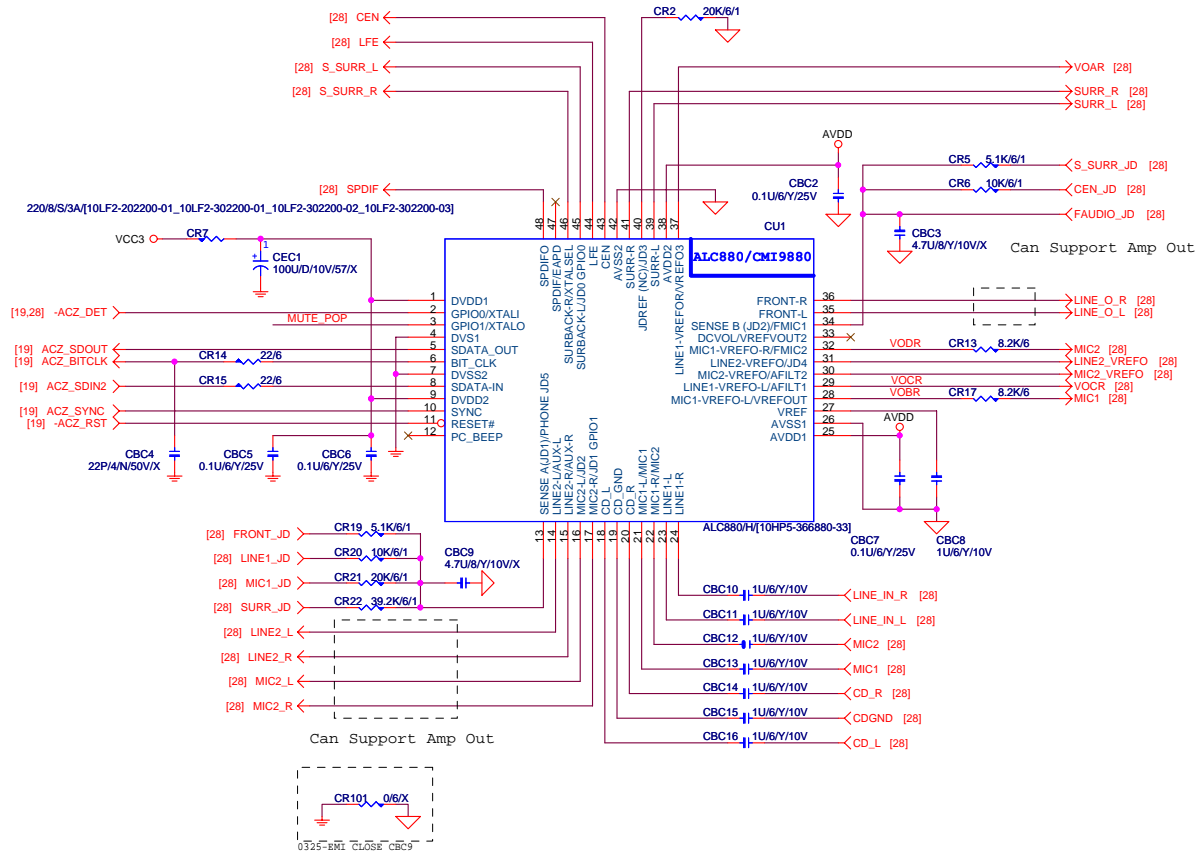
KB/MS



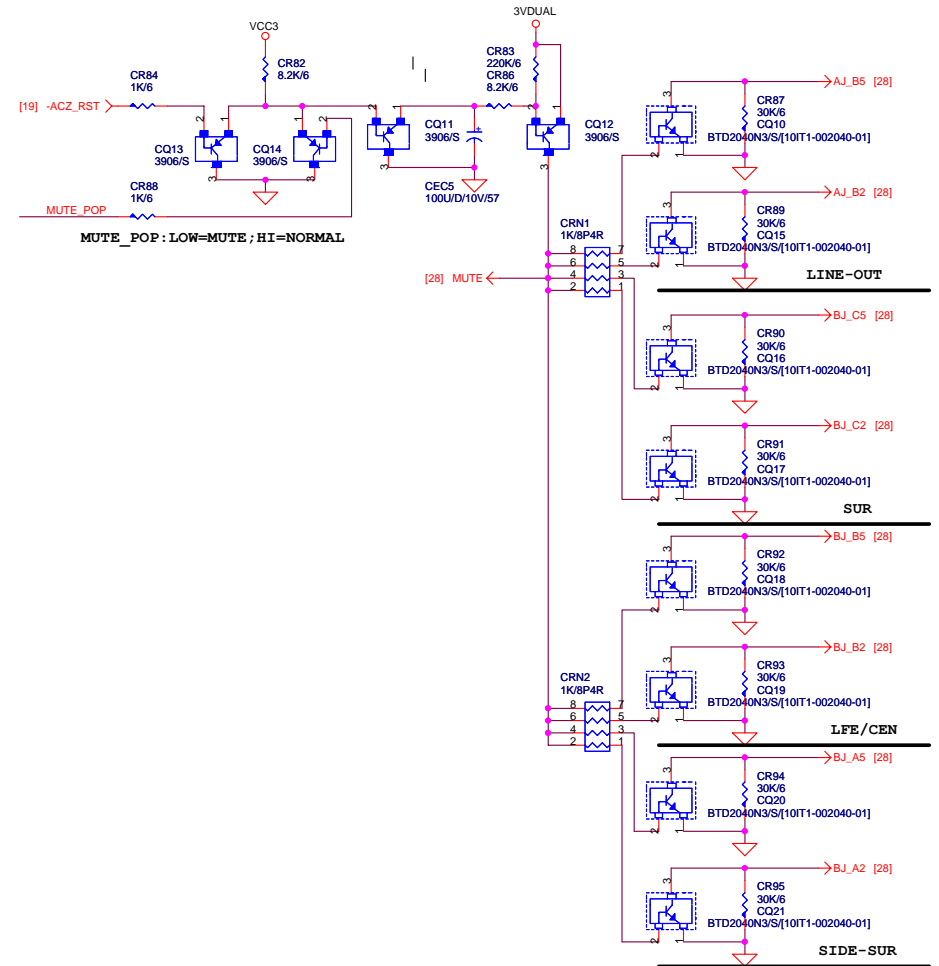
FWM BIOS



AZALIA CODEC



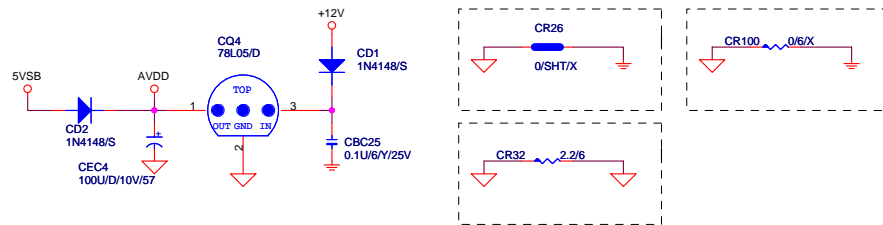
ANTI-POP NOISE



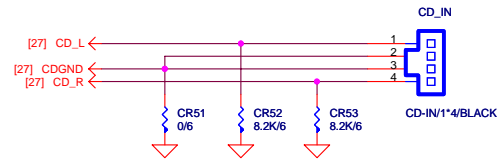
Gigabyte Technology

Title			
AC97 ALC658			
Size	Document Number		Rev
Custom	81945AE-AE		1.1
Date:	Friday, June 10, 2005	Sheet	27 of 34

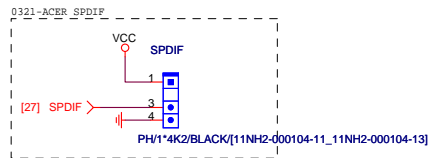
CODEC POWER/EMI PAD



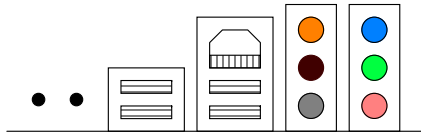
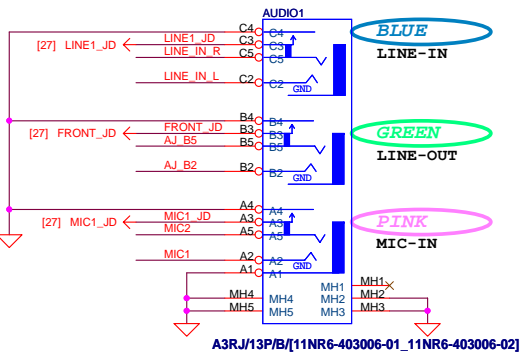
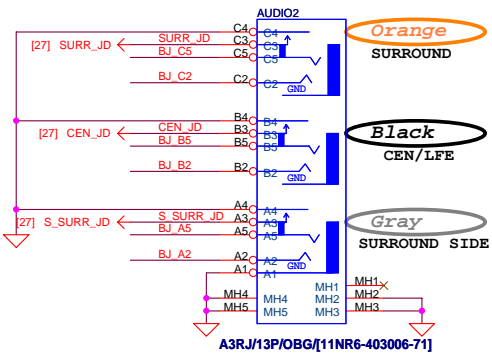
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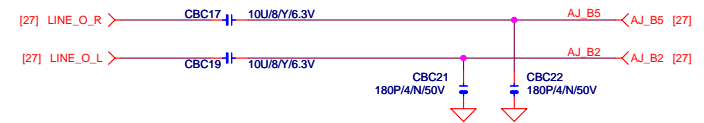
SPDIF



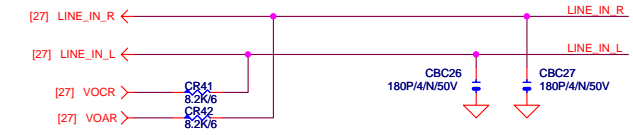
AZALIA JACK ACER REQUEST CHANGE LOCTION

0321
NEW TYPE: IBM_AUDIO-13P-1

LINE-OUT



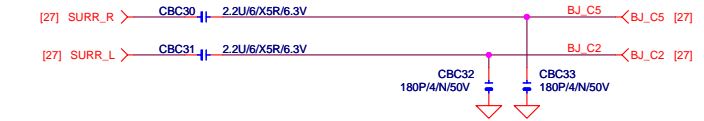
LINE-IN



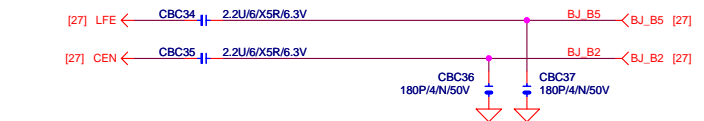
MIC-IN



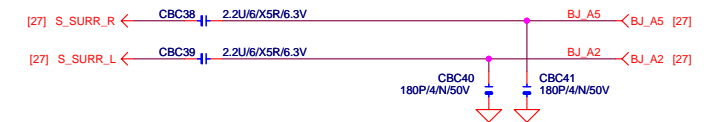
SURROUND



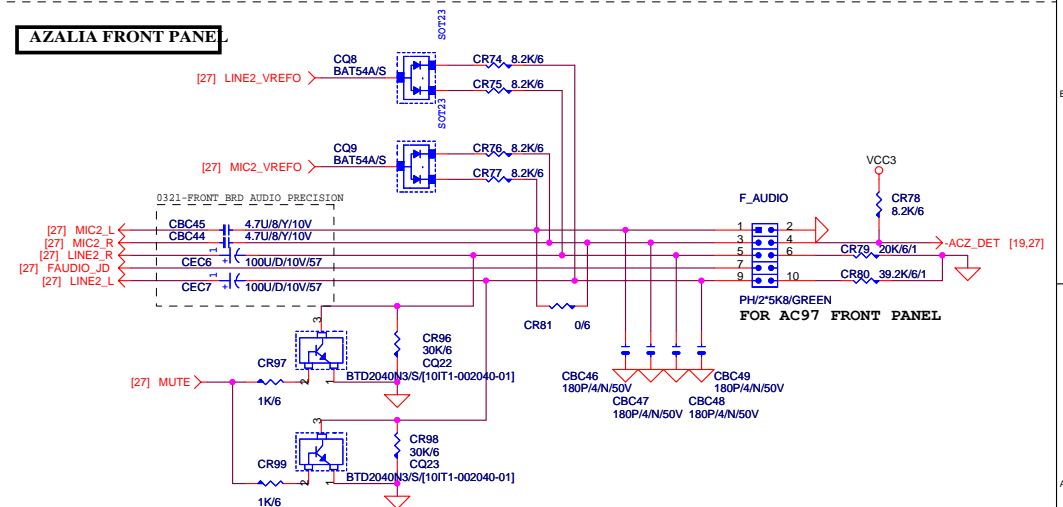
CEN/LFE



SURR BACK

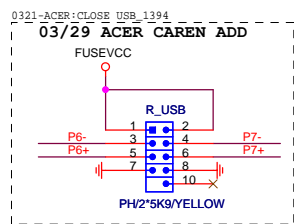
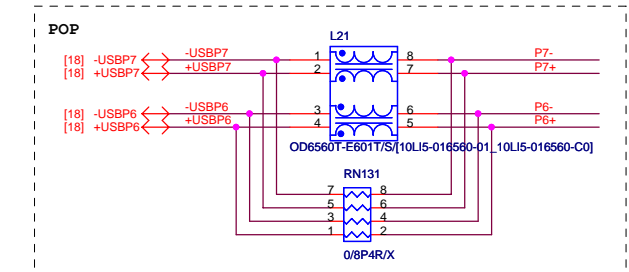
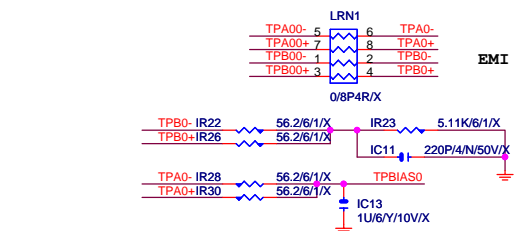
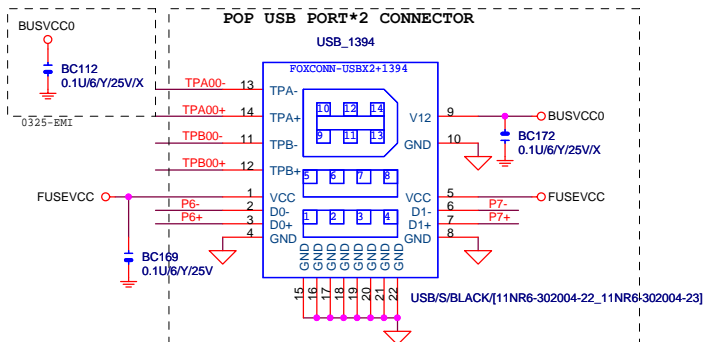
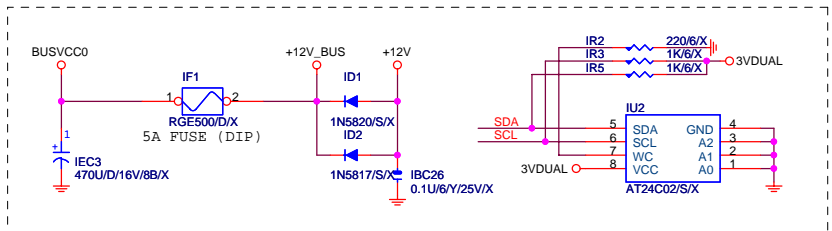
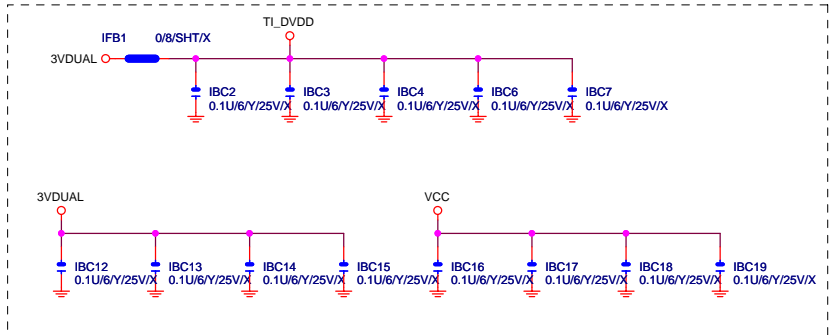


AZALIA FRONT PANE



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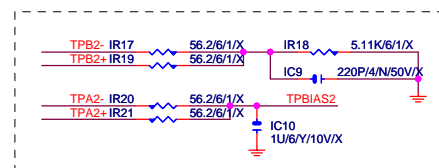
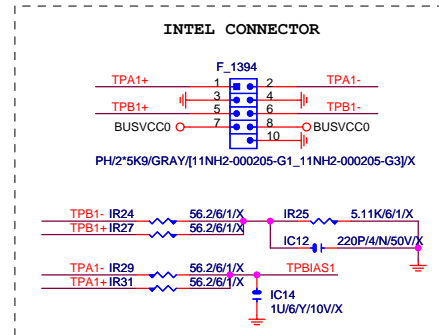
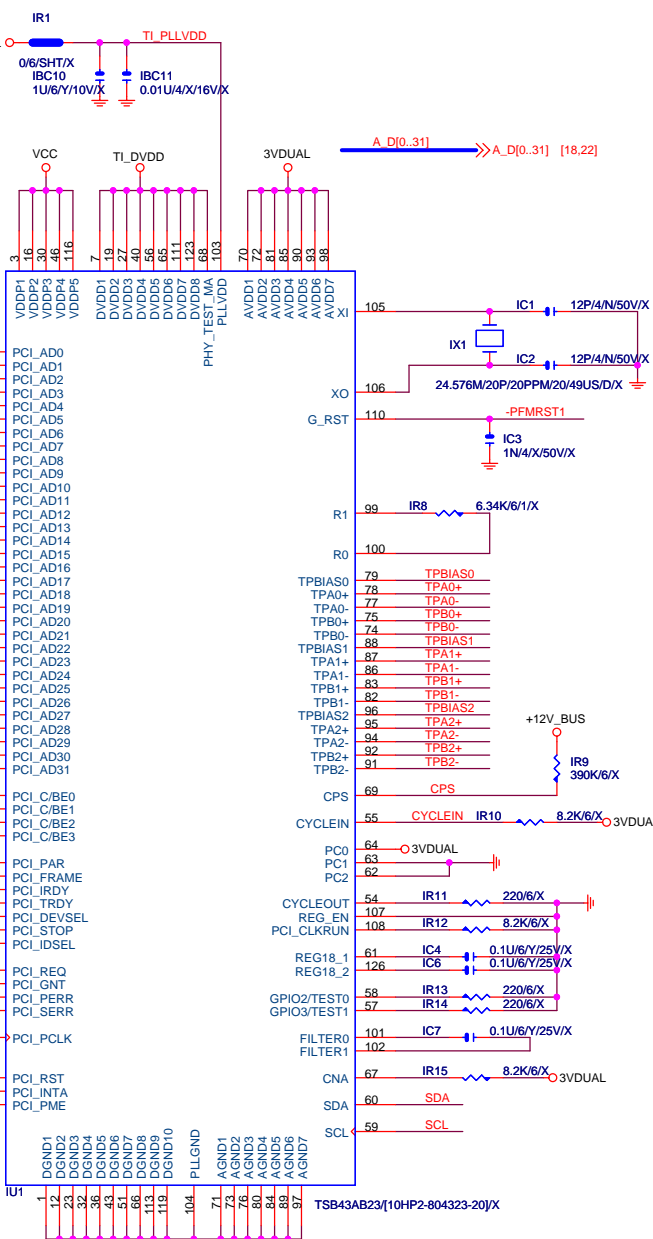
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- | | | |
|-------|-----|----------|
| A D0 | 52 | PCI_AD0 |
| A D1 | 50 | PCI_AD1 |
| A D2 | 49 | PCI_AD2 |
| A D3 | 48 | PCI_AD3 |
| A D4 | 47 | PCI_AD4 |
| A D5 | 45 | PCI_AD5 |
| A D6 | 44 | PCI_AD6 |
| A D7 | 42 | PCI_AD7 |
| A D8 | 39 | PCI_AD8 |
| A D9 | 38 | PCI_AD9 |
| A D10 | 37 | PCI_AD10 |
| A D11 | 35 | PCI_AD11 |
| A D12 | 34 | PCI_AD12 |
| A D13 | 33 | PCI_AD13 |
| A D14 | 31 | PCI_AD14 |
| A D15 | 29 | PCI_AD15 |
| A D16 | 14 | PCI_AD16 |
| A D17 | 13 | PCI_AD17 |
| A D18 | 11 | PCI_AD18 |
| A D19 | 10 | PCI_AD19 |
| A D20 | 9 | PCI_AD20 |
| A D21 | 8 | PCI_AD21 |
| A D22 | 6 | PCI_AD22 |
| A D23 | 5 | PCI_AD23 |
| A D24 | 128 | PCI_AD24 |
| A D25 | 127 | PCI_AD25 |
| A D26 | 125 | PCI_AD26 |
| A D27 | 124 | PCI_AD27 |
| A D28 | 122 | PCI_AD28 |
| A D29 | 121 | PCI_AD29 |
| A D30 | 120 | PCI_AD30 |
| A D31 | 118 | PCI_AD31 |

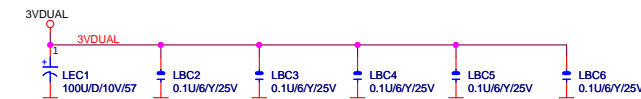
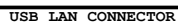
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|-----------------|----|------------|
| [18,22] -C_BE0 | 41 | PCI_C/BE0 |
| [18,22] -C_BE1 | 28 | PCI_C/BE1 |
| [18,22] -C_BE2 | 15 | PCI_C/BE2 |
| [18,22] -C_BE3 | 2 | PCI_C/BE3 |
| [18,22] PAR | 26 | PCI_PAR |
| [18,22] -FRAME | 17 | PCI_FRAME |
| [18,22] -IRDY | 18 | PCI_IRDY |
| [18,22] -TRDY | 20 | PCI_TRDY |
| [18,22] -DEVSEL | 21 | PCI_DEVSEL |
| [18,22] -STOP | 22 | PCI_STOP |
| A D23 | 4 | PCI_IDSEL |

- | | | |
|---------------------|-----|----------|
| [18,22] -REQ2 | 115 | PCI_REQ |
| [18] -GNT2 | 114 | PCI_GNT |
| [18,22] -PERR | 24 | PCI_PERR |
| [18,22] -SERR | 25 | PCI_SERR |
| [21] 1394CLK | 112 | PCI_PCLK |
| [24,26,30] -PFMRST1 | 53 | PCI_RST |
| [18,22] -PIRQH | 109 | PCI_INTA |
| [18,22,30] -PCIPME | 117 | PCI_PME |



Layout Check 注意事項

- [14,15,17,19,21,22,24]
[14,15,17,19,21,22,24]



2.5V

3VDUAL

LBC8
0.1uF/5V/25V

LR4
4.7K/6

VDD25

HM7722-01

LQ1
HM7722-01

CTRL25_P

AVDD25L

VDD25

LBC16
0.1uF/5V/25V

LBC9
0.1uF/5V/25V

LBC10
0.1uF/5V/25V

LBC11
0.1uF/5V/25V

LBC12
0.1uF/5V/25V

LBC13
0.1uF/5V/25V

LFB1
0/8

AVDD25

LBC14
0.1uF/5V/25V

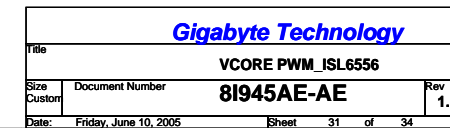
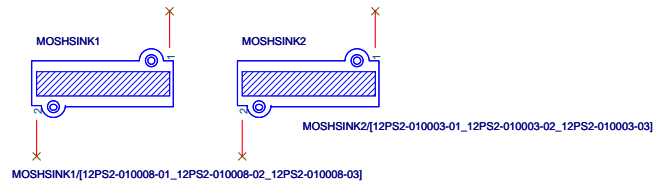
LFB2
0/8

LEC2
100uF/D10V/57

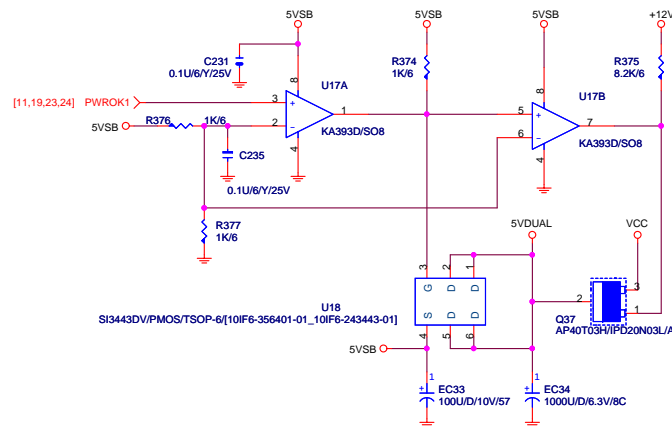
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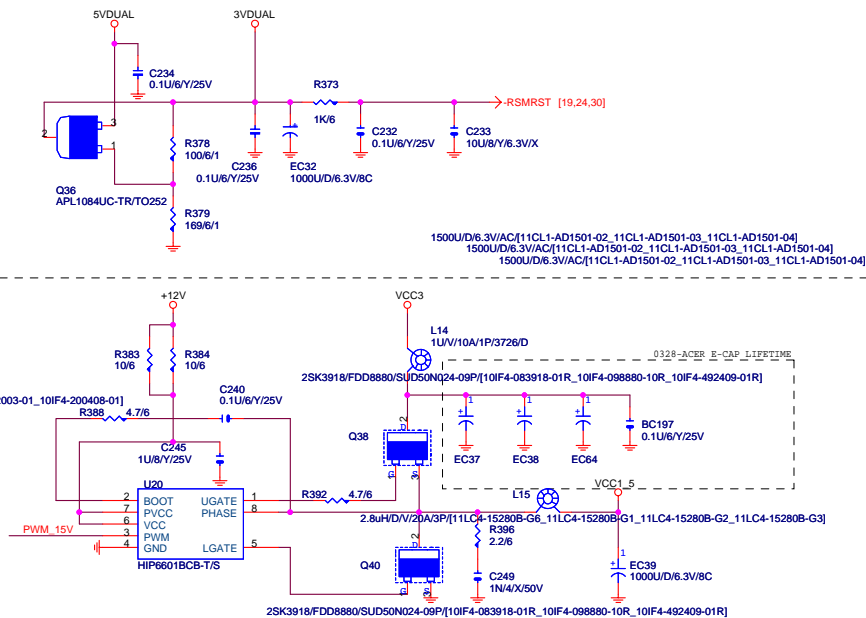
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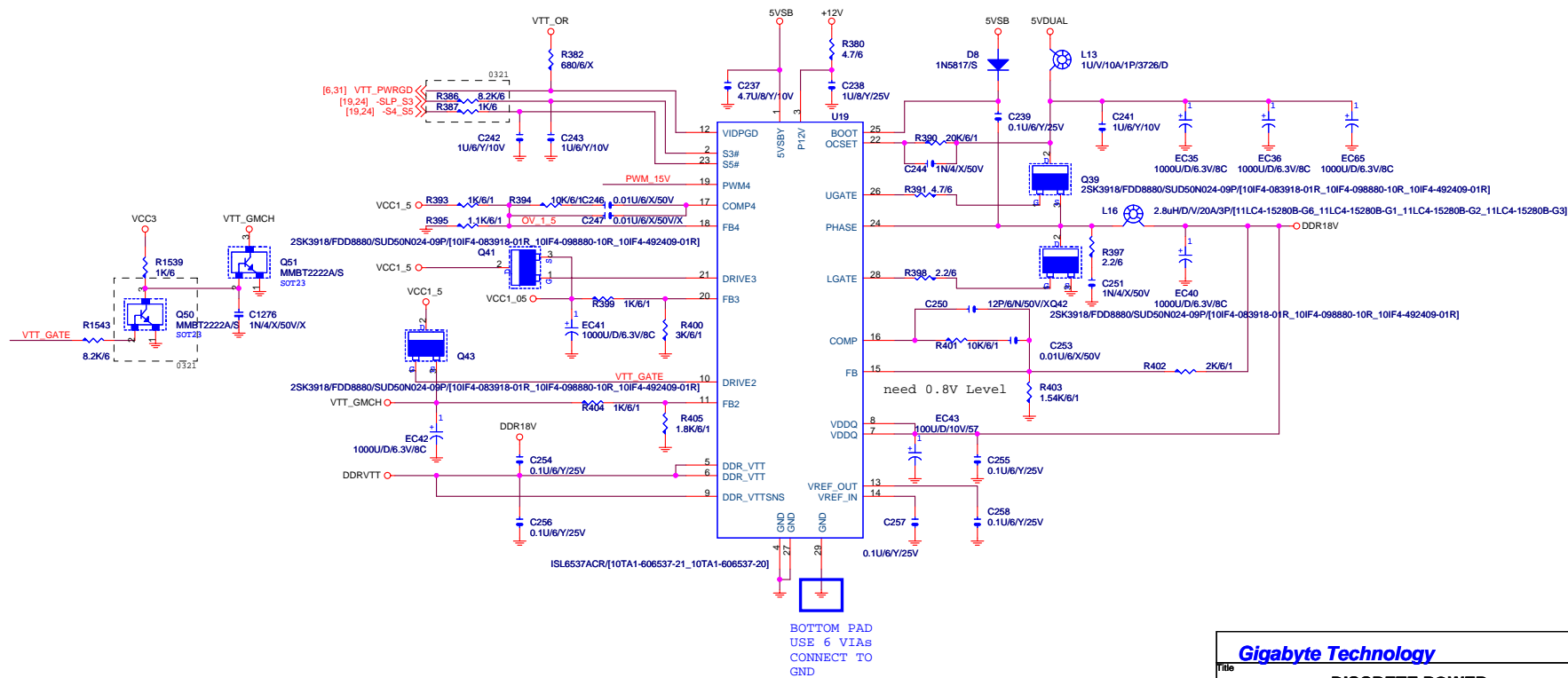
5VDUAL



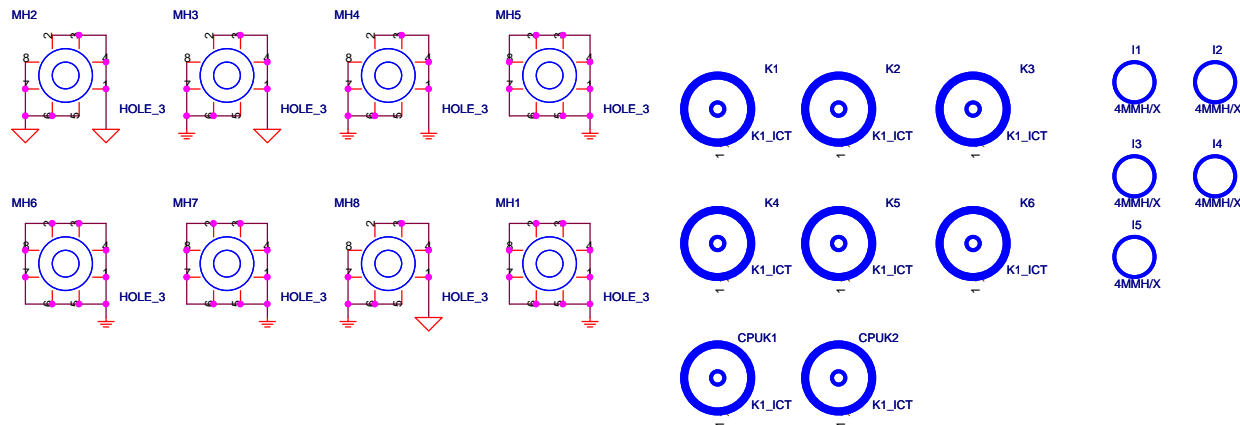
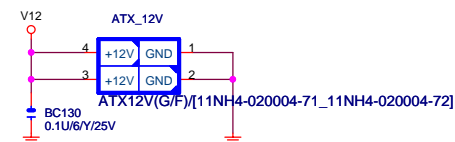
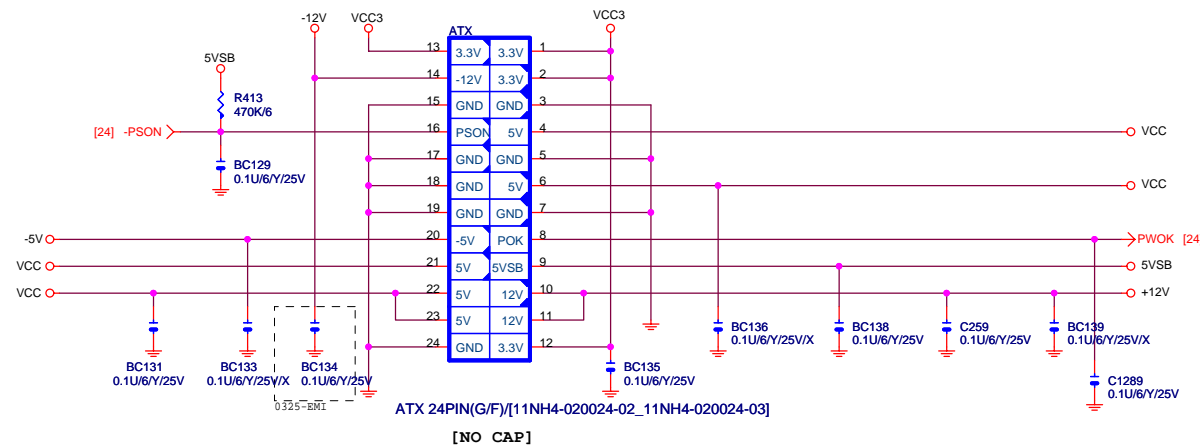
3VDUAL



VCC1	5
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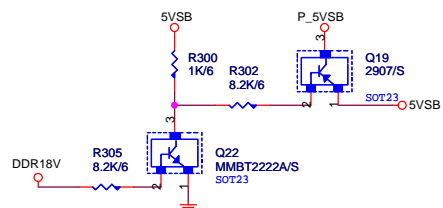
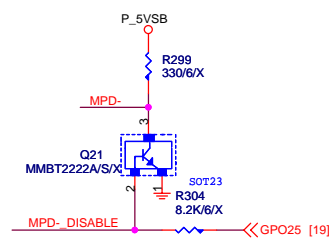
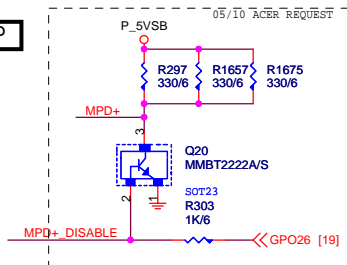


ATX POWER CONNECTOR

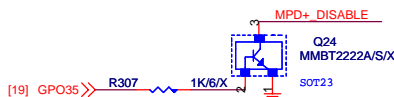


DUAL COLOR LED

0321

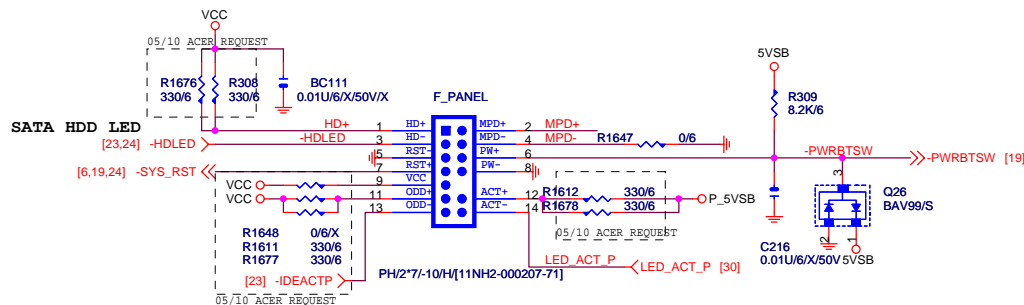


PREVENT S0 W/O CPU, GREEN IS ON.

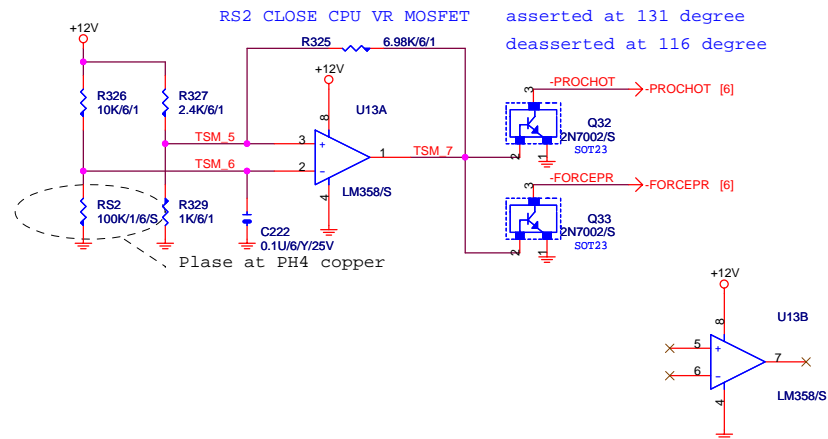


INTEL FRONT PANEL

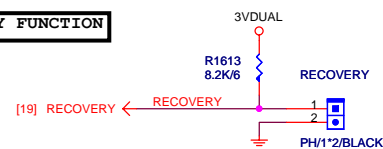
modify PIN2*7 cut 10pin for ACER



PROCESSOR HOT



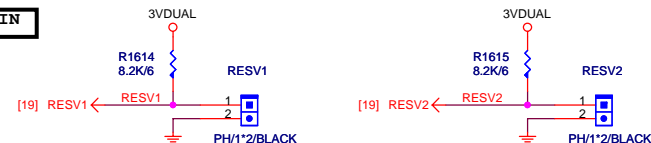
RECOVERY FUNCTION



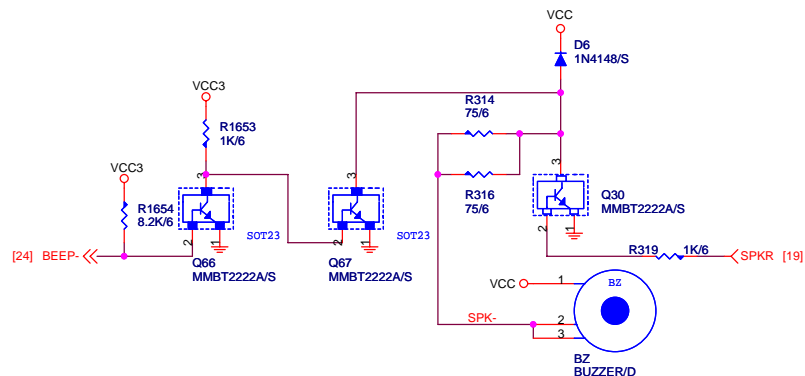
RECOVERY

OPEN	NORMAL
SHORT	RECOVERY

RESERVE PIN



BUZZER



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